LCFC Confidential

CGx20 M/B Schematics Document

NM-A805 REV:0.1

Intel Braswell M-Processor with DDRIIIL

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2015-10-09

REV:0.1

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Title	Cover Page		
Size: Custom	Document Number: CGx20		Rev: 0.1
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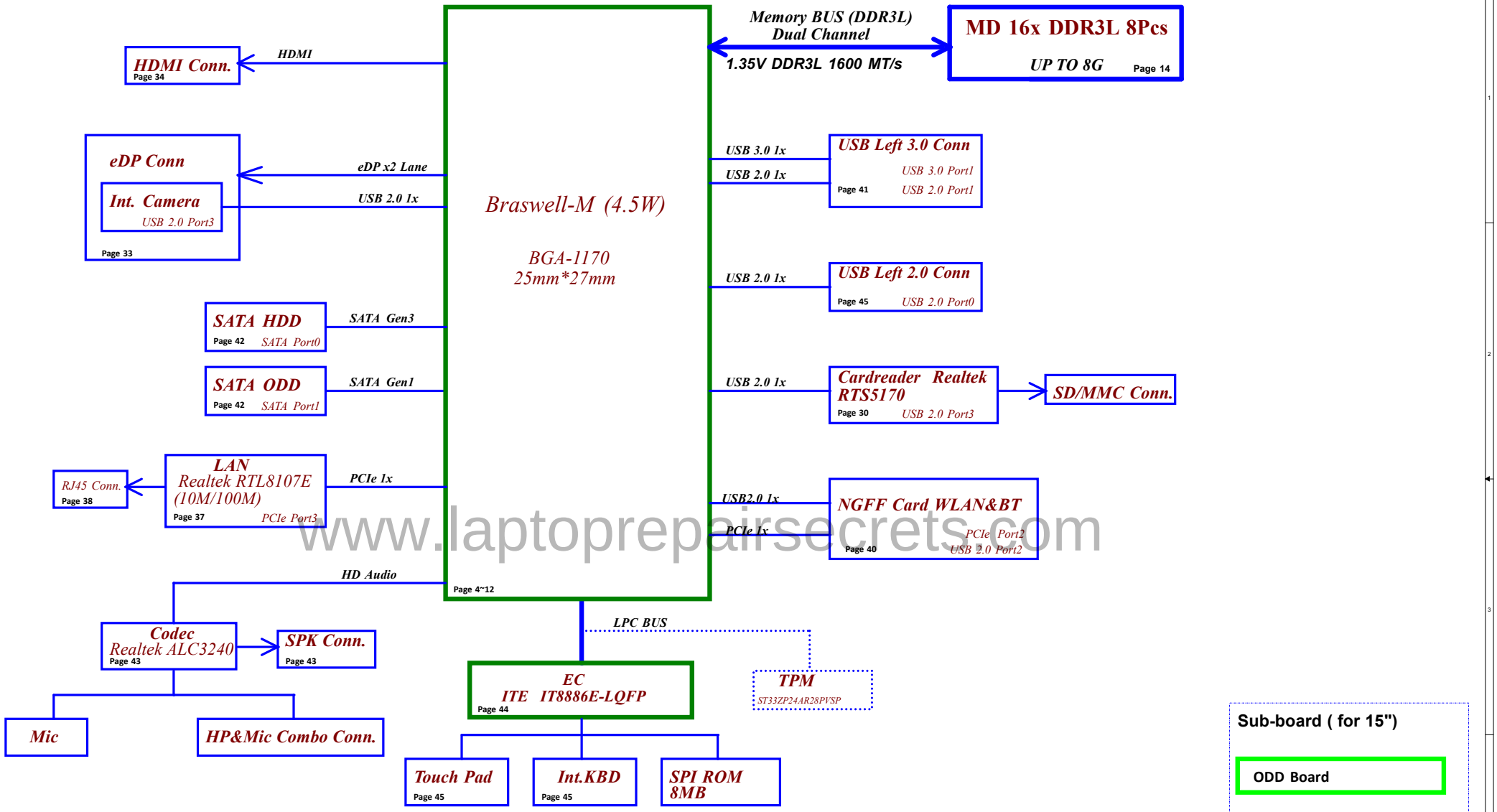

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Title: **Block Diagram**

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Voltage Rails (O --> Means ON , X --> Means OFF)

State \ Power Plane	B+ +3VL	+3VALW +5VALW	+3VALW_SOC +1.0VALW +1.8VALW	+1.35V	+5VS +3VS +1.5VS +0.68VS CPU_CORE GFX_CORE
S0	O	O	O	O	O
S3	O	O	O	O	X
S5 S4/AC Only	O	O	O	X	X
S5 S4 Battery only	O	X	X	X	X
S5 S4 AC & Battery don't exist	X	X	X	X	X

STATE \ SIGNAL	SLP_S1#	SLP_S3#	SLP_S4#	+VALW	+VALW_PCH	+V	+VS	Clock
Full ON	HIGH	HIGH	HIGH	ON	ON	ON	ON	ON
S1(Power On Suspend)	LOW	HIGH	HIGH	ON	ON	ON	ON	LOW
S3 (Suspend to RAM)	LOW	LOW	HIGH	ON	ON	ON	OFF	OFF
S4 (Suspend to Disk)	LOW	LOW	LOW	ON	ON	OFF	OFF	OFF
S5 (Soft OFF)	LOW	LOW	LOW	ON	ON	OFF	OFF	OFF

SMBUS Control Table

	SOURCE	VGA	BATT	IT8986E	SODIMM	WLAN WiMAX	Thermal Sensor	PCH	TP Module	charger
EC_SMB_CK1 EC_SMB_DA1	EC +3VALW	X	V	V +3VALW	X	X	X	X	X	V
EC_SMB_CK2 EC_SMB_DA2	EC +3VS	V +3VGS	X	V +3VS	X	X	V +3VS	X	X	X
PCH_SMB_CLK PCH_SMB_DATA	PCH +3VALW_PCH	X	X	X	V +3VS	V +3VS	X	V +3VALW_PCH	X	X

STATE \ SIGNAL	SLP_S1#	SLP_S3#	SLP_S4#	+VALW	+V	+VS	Clock
Full ON	HIGH	HIGH	HIGH	HIGH	ON	ON	ON	ON
S1(Power On Suspend)	LOW	HIGH	HIGH	HIGH	ON	ON	ON	LOW
S3 (Suspend to RAM)	LOW	LOW	HIGH	HIGH	ON	ON	OFF	OFF
S4 (Suspend to Disk)	LOW	LOW	LOW	HIGH	ON	OFF	OFF	OFF
S5 (Soft OFF)	LOW	LOW	LOW	LOW	ON	OFF	OFF	OFF

USB Port Table

XHCI	Port	Port device
USB 3.0	0	USB Port (Left Side)
	1	USB3.0 Card Reader
USB 2.0	0	USB Port (Left Side)
	1	USB Port (Right Side)
	2	BT
	3	Camera
	4	USB Port (Right Side)
USB HUB	1	
	2	
	3	
	4	

PCIE PORT LIST

Port	Device
1	Discrete GPU
2	Discrete GPU
3	WLAN
4	LAN
5	
6	
7	
8	

BOM Structure Table

BOM Structure	BTO Item
AOAC@	AOAC support part
OPT@	GPU Part
UMA@	UMA SKU ID part
14@	For 14" part
15@	For 15" part
100M@	100M LAN part
N15SGT@	N15SGT Part
N15VGM@	N15GSM Part
GIGA@	GIGA LAN Part
GC6@	GPU GC6 Part
TS@	Touch Screen part
RANKA@	GPU VRAM RANKA PART
RANKB@	GPU VRAM RANKB PART
ME@	Connector
CD@	COST DOWN
@	Not stuff
H4T@	Hynix VRAM Part
M4T@	Micron VRAM Part
S4T@@	Samsung VRAM Part

DDI PORT LIST

Port	Device
DDI0	DP TO VGA
DDI1	eDP
DDI2	HDMI

EC SM Bus1 address

Device	
Smart Battery	0001 011X b
Charger	need to update

EC SM Bus2 address

Device	Address
Thermal Sensor EMC1403-2	1001_100xb(reserve)
VGA	0x9E(base on NV default)

PCH SM Bus address

Device	Address
DDR DIMMA	1010 000Xb
Wlan	Rsvd
TP	need to update

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Title: **Notes List**

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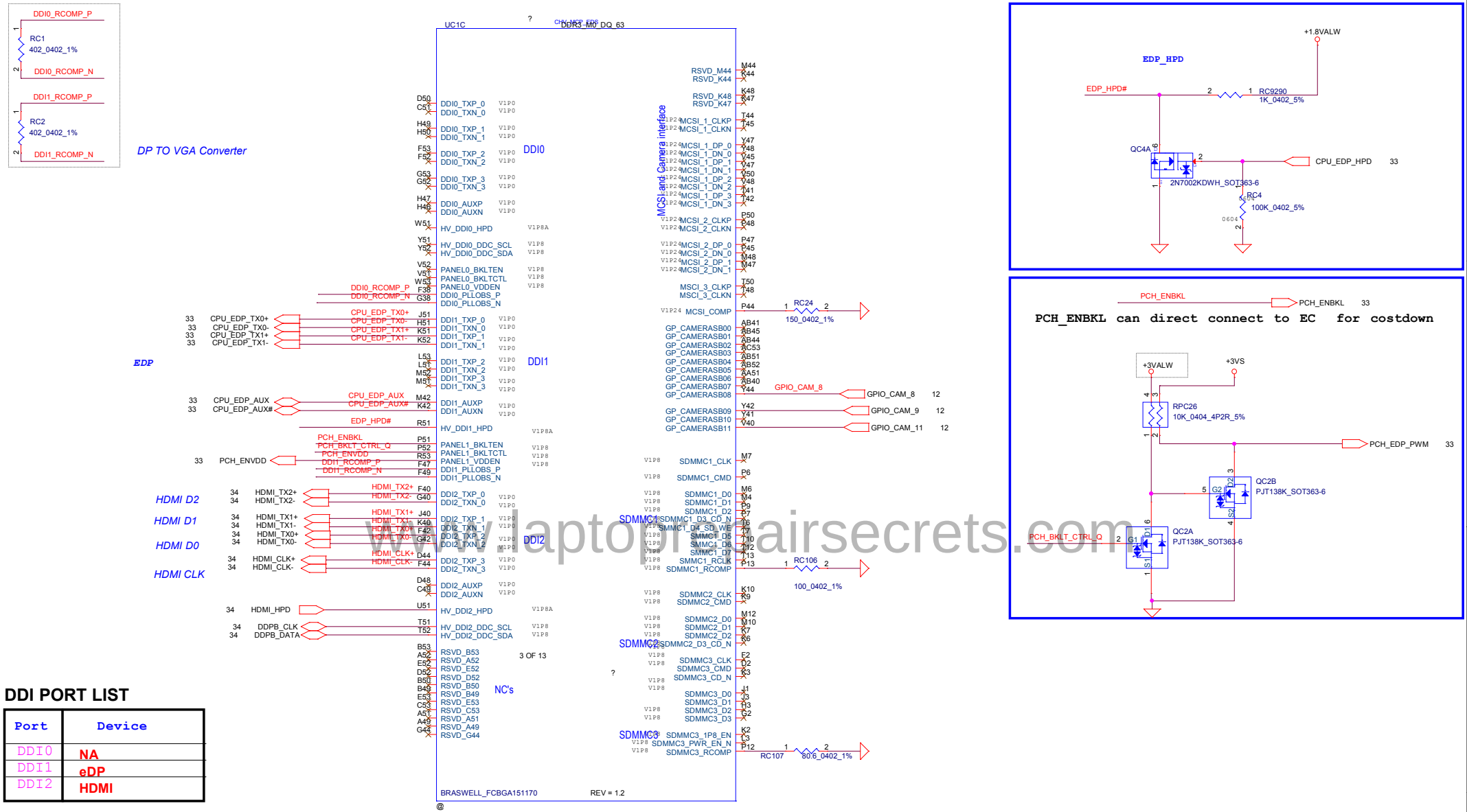

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DDI PORT LIST

Port	Device
DDI0	NA
DDI1	eDP
DDI2	HDMI

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Title: SOC (DDI,EDP,HDMI)

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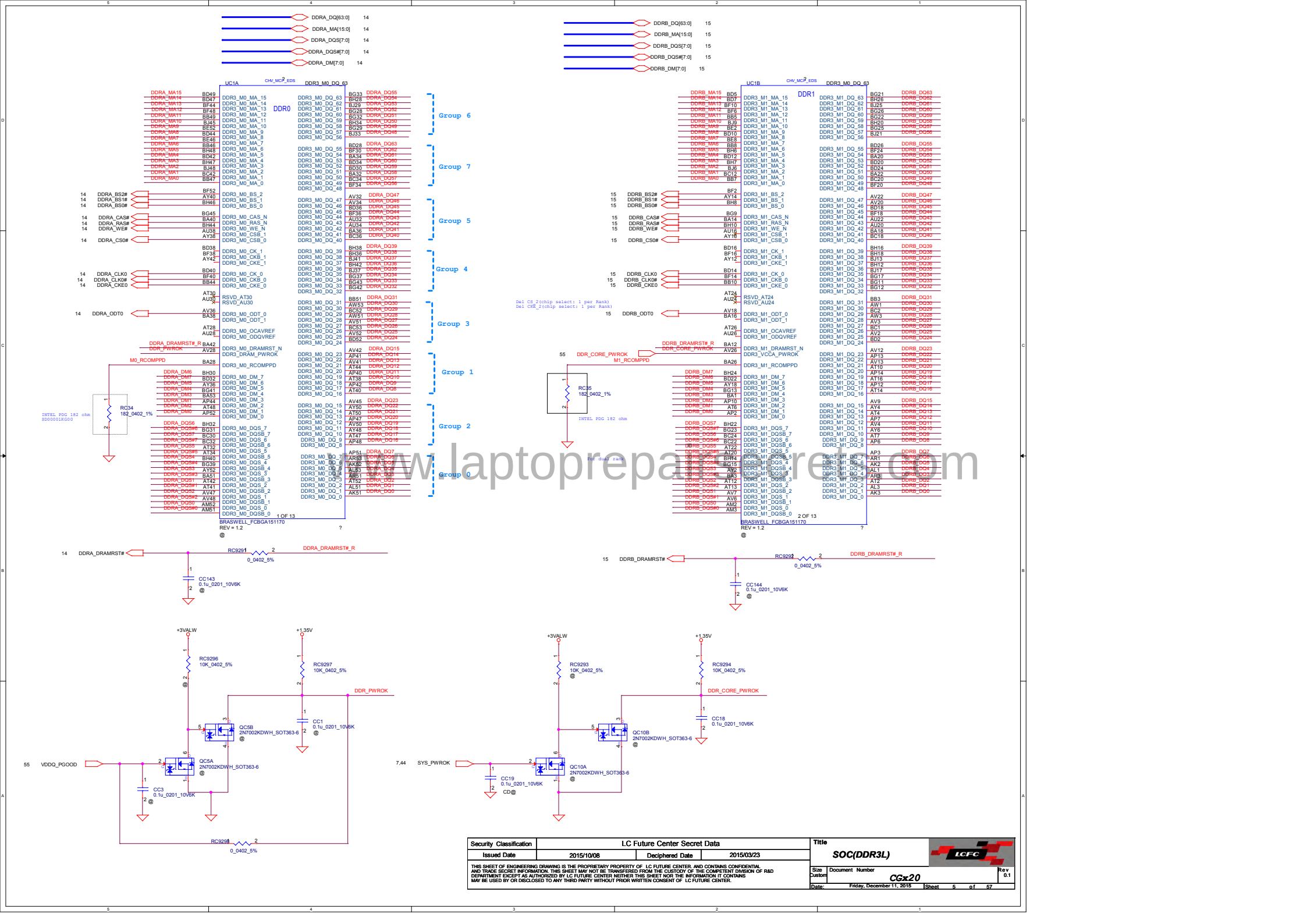

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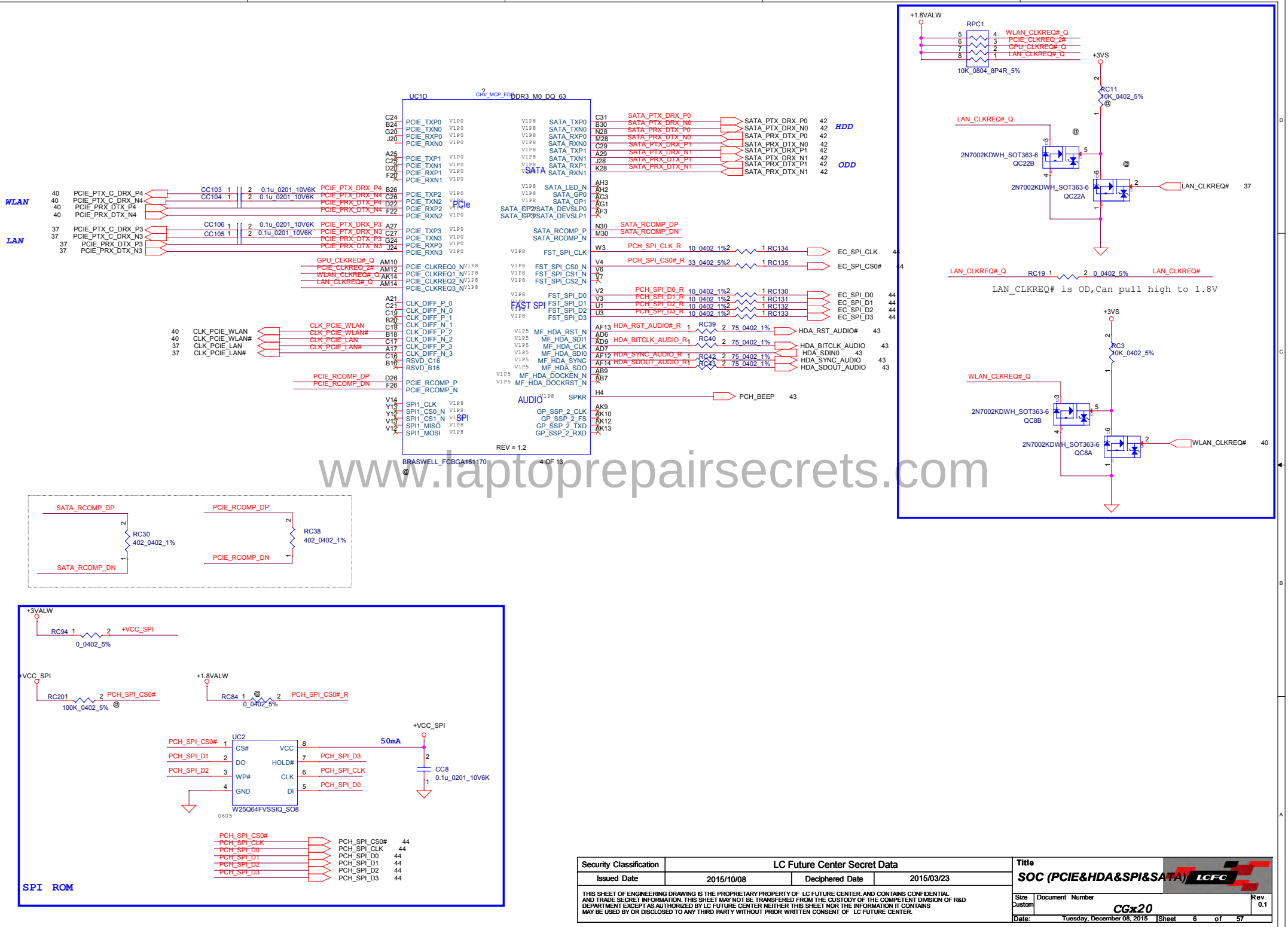

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Title: SOC (PCIE&HDA&SPI&SATA)

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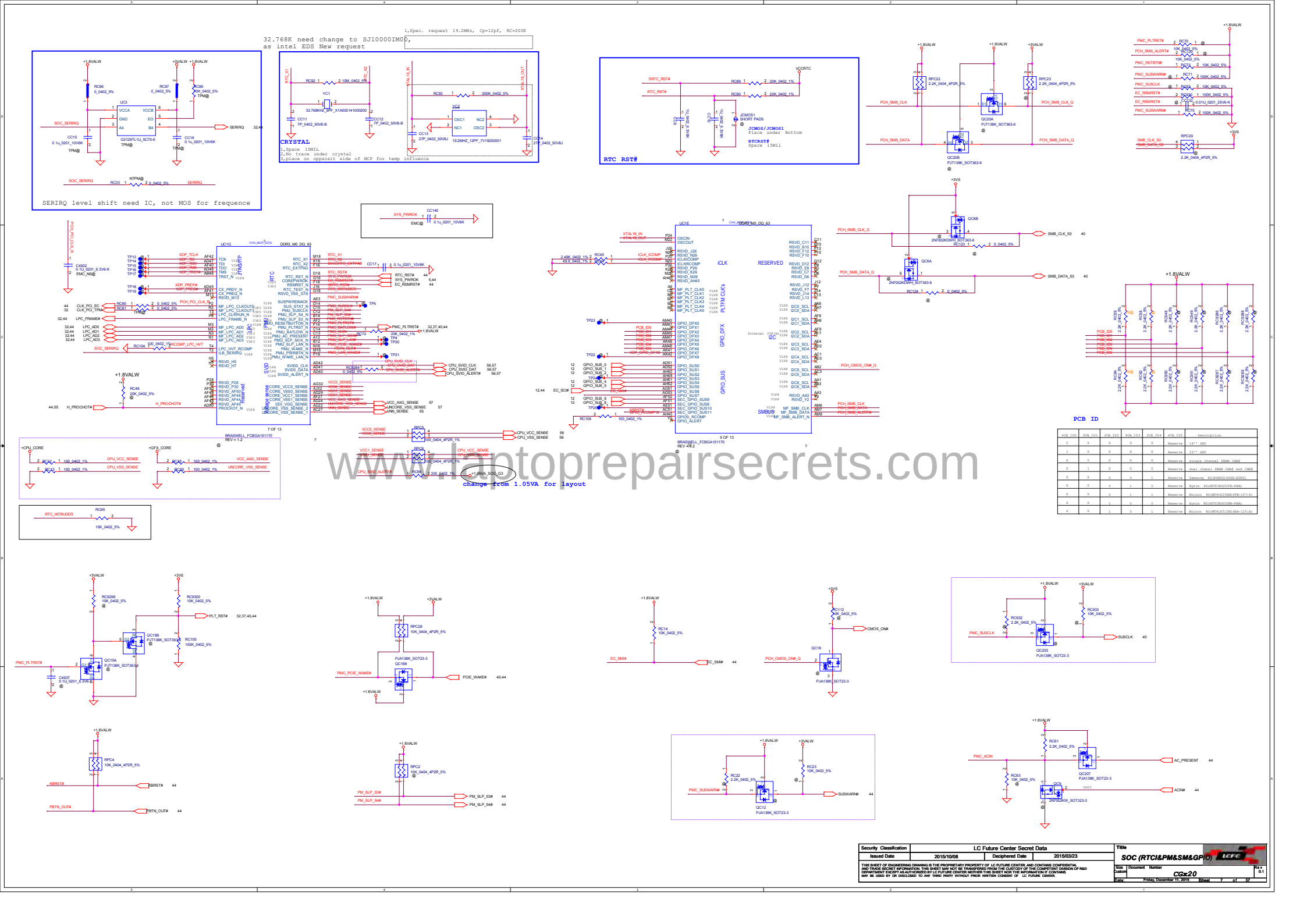

PCB_ID0	PCB_ID1	PCB_ID2	PCB_ID3	PCB_ID4	PCB_ID5	Description
0	X	X	X	X	X	Reserve 14'' SKU
1	X	X	X	X	X	Reserve 15'' SKU
X	0	X	X	X	X	Reserve single channel DRAM CHA8
X	1	X	X	X	X	Reserve dual channel DRAM CHA8 and CHB8
X	X	0	0	1	X	Reserve Samsung 4G(K4B4G0146Q-BYK0)
X	X	0	1	0	X	Reserve Hynix 4G(H5TC4G63CFR-PBA)
X	X	0	1	1	X	Reserve Micron 4G(MT41K256M16TW-107:P)
X	X	1	0	0	X	Reserve Hynix 8G(H5TC8G63CMR-PBA)
X	X	1	0	1	X	Reserve 8G(MT41K512M16HA-125:A)

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Title: SOC (RTC&PM&SM&GPIO)

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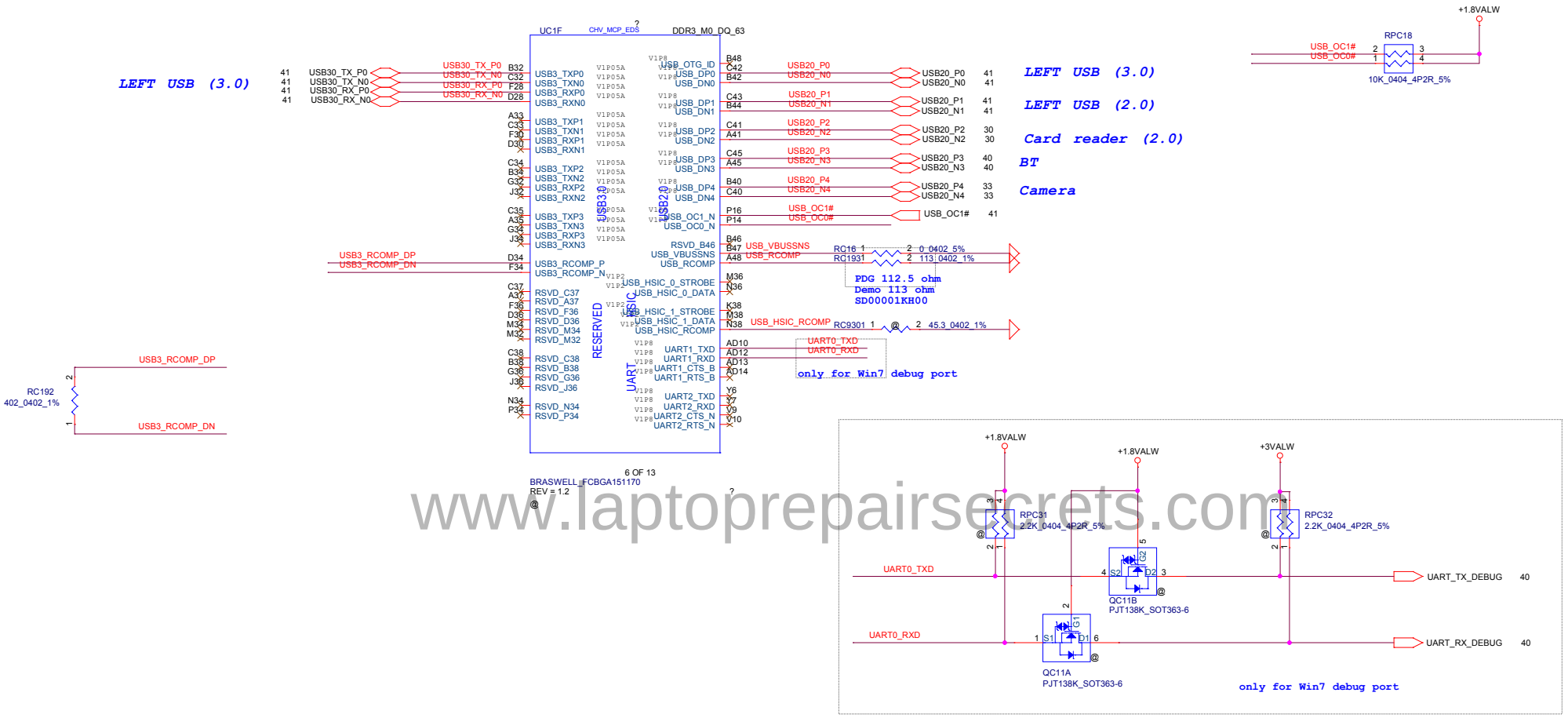

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Title	SOC (USB)		
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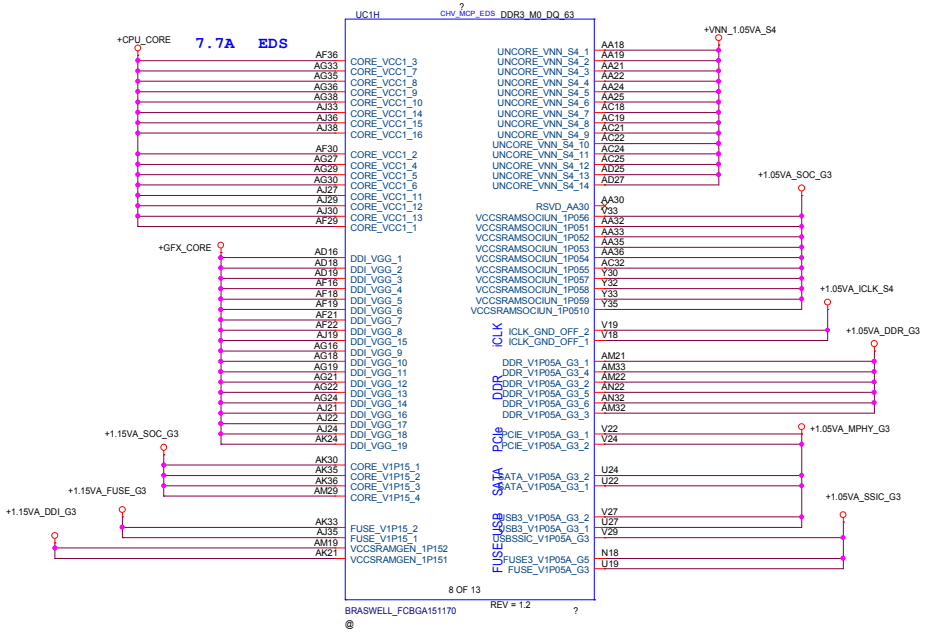

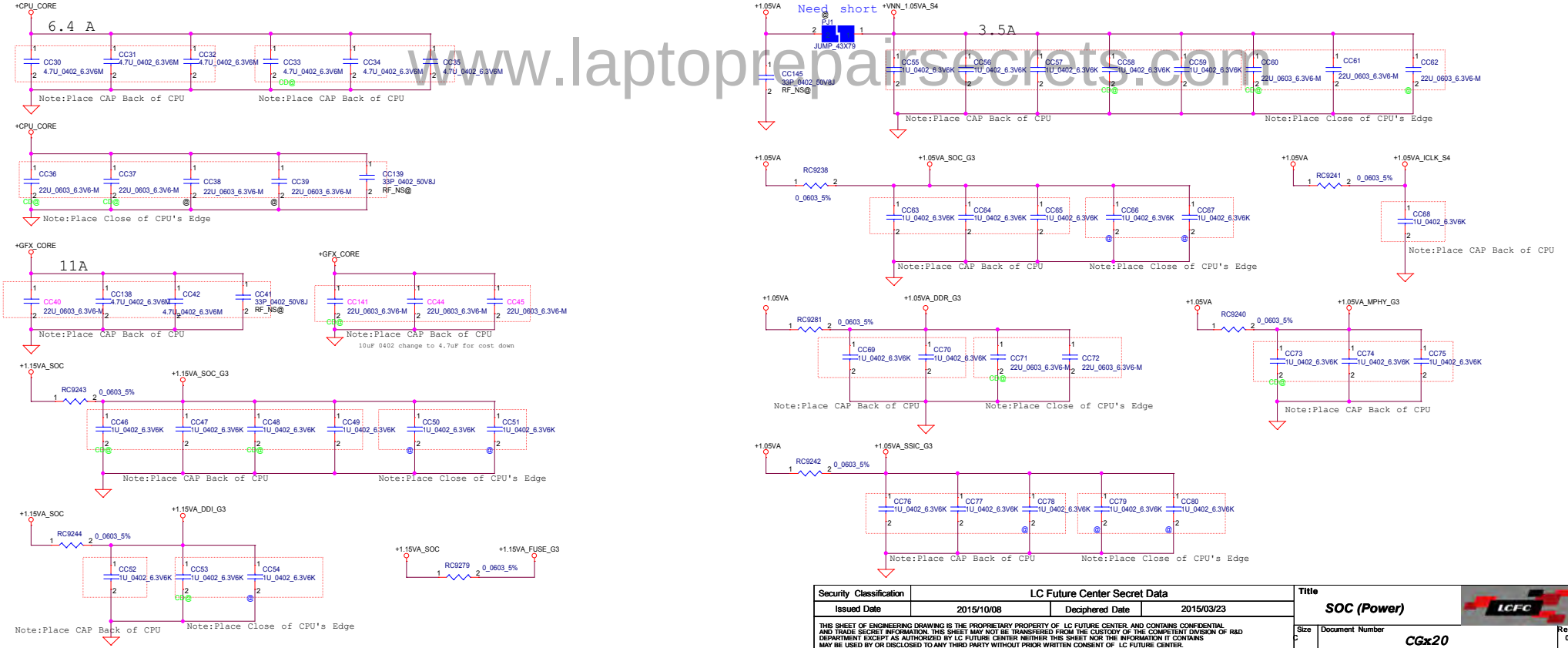

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Title: **SOC (Power)**

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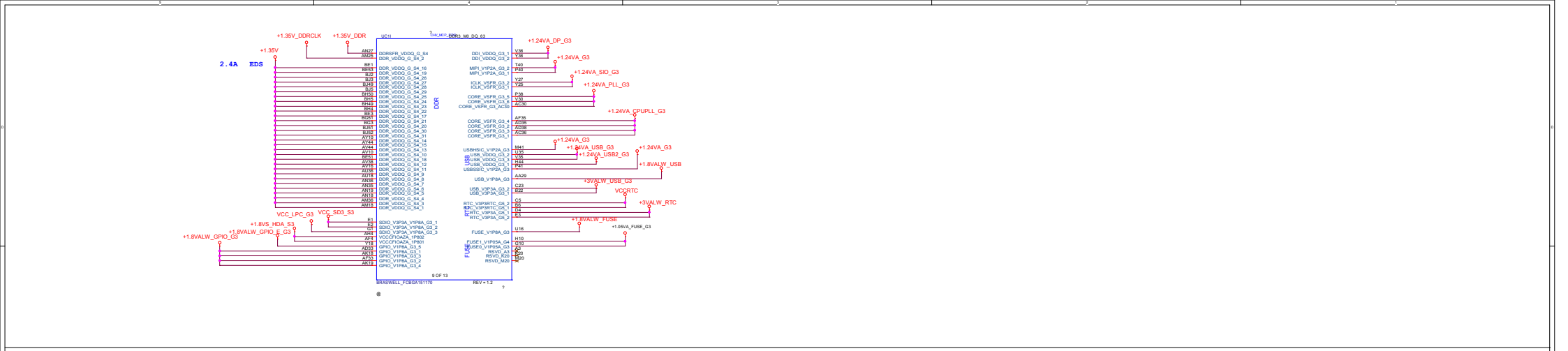

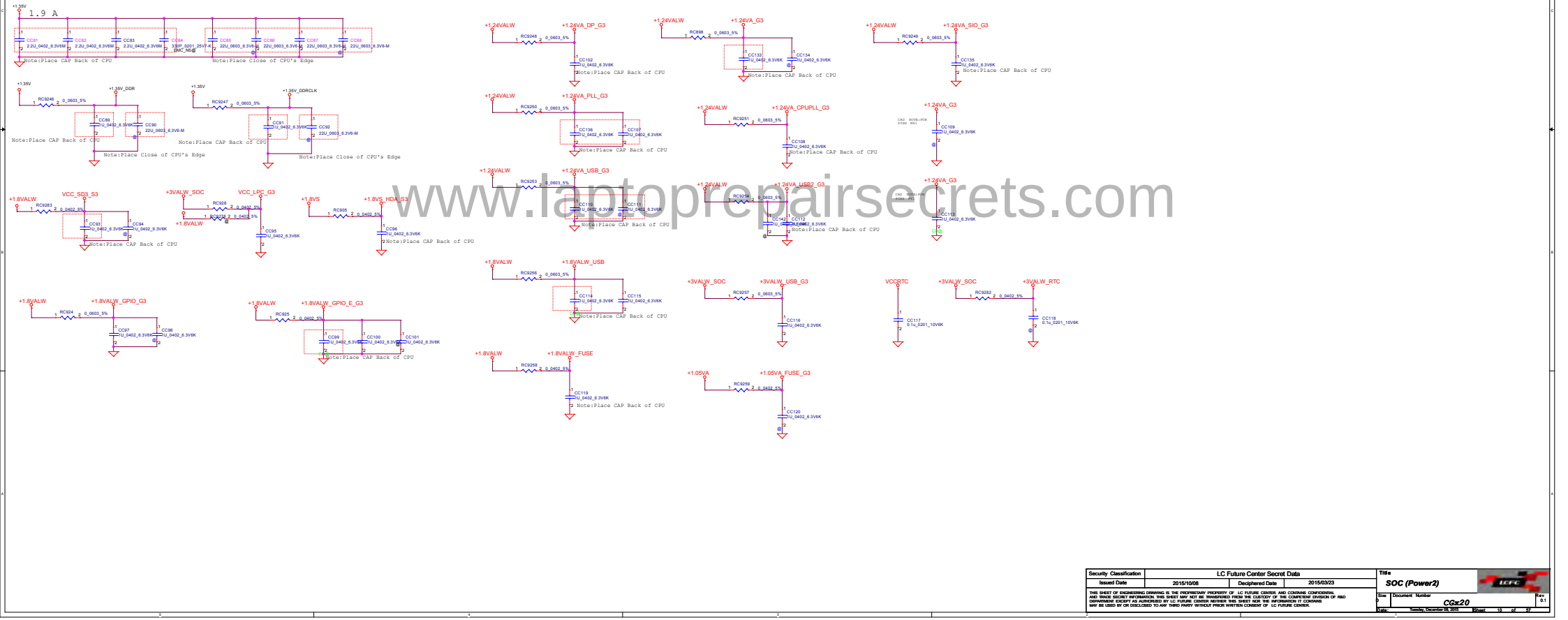

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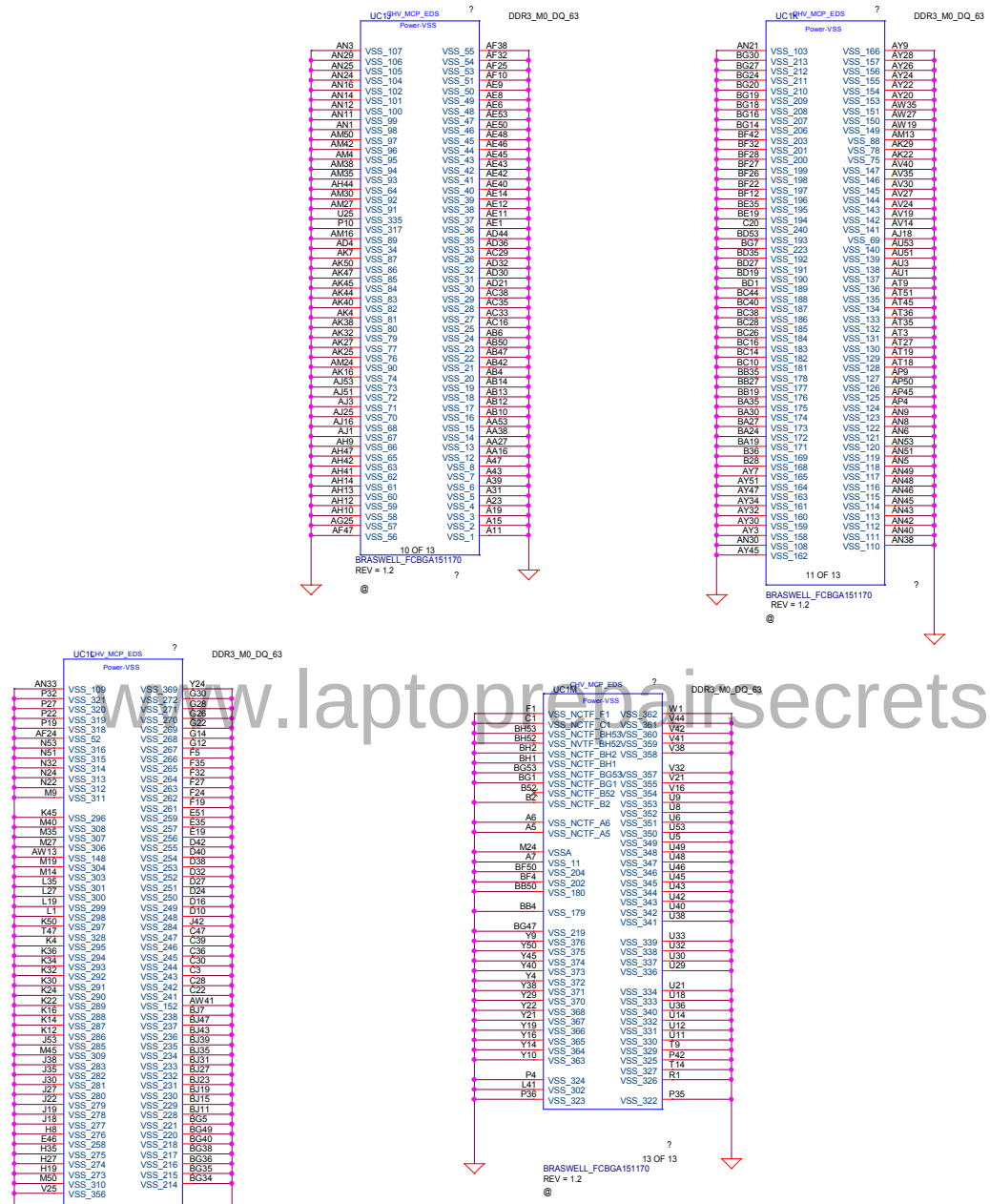

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Title: **SOC (VSS)**

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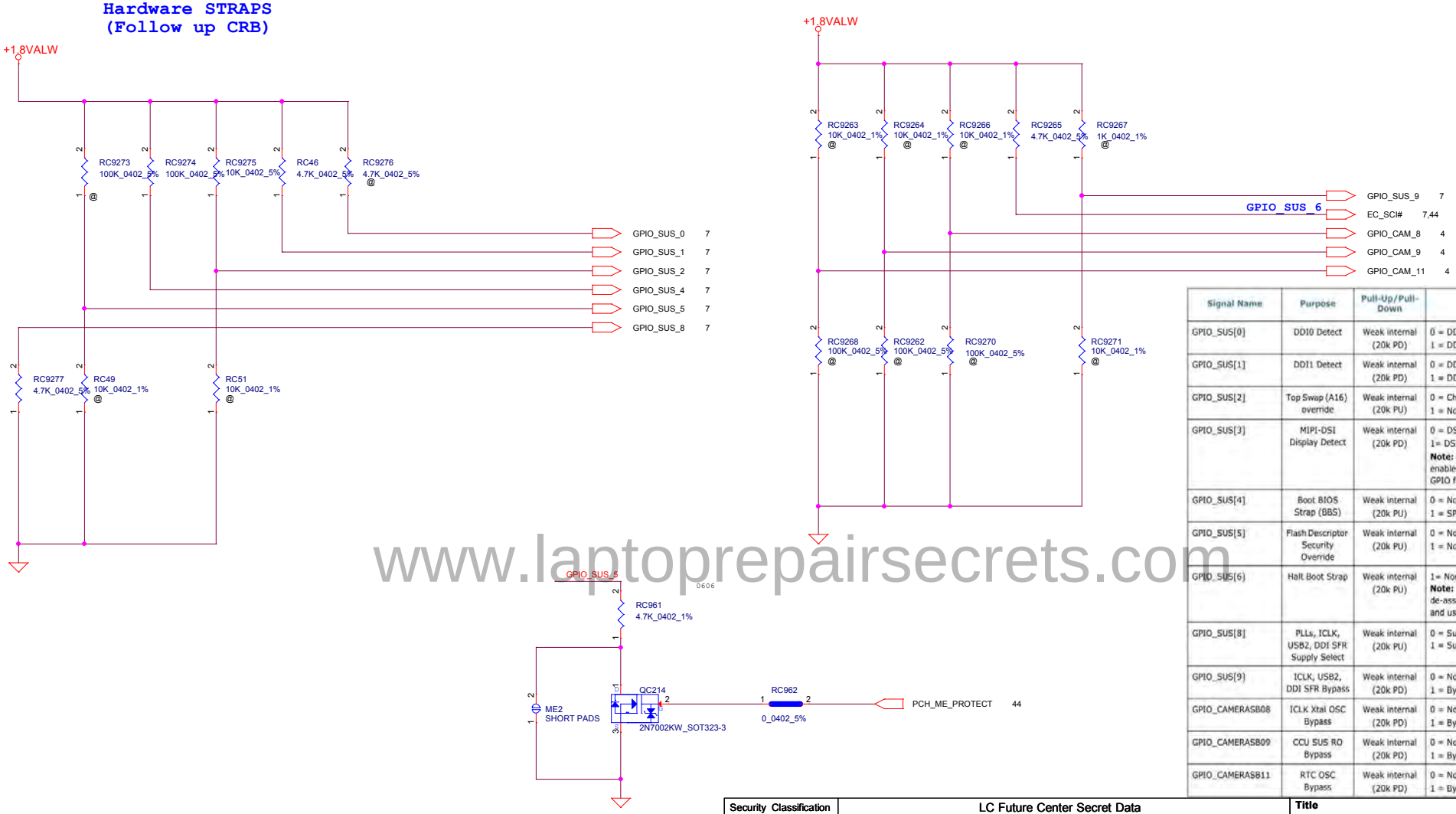
Hardware STRAPS (Follow up CRB)



Signal Name	Purpose	Pull-Up/Pull-Down	Strap Description
GPIO_SUS[0]	DDI0 Detect	Weak internal (20k PD)	0 = DDI0 not detected 1 = DDI0 detected
GPIO_SUS[1]	DDI1 Detect	Weak internal (20k PD)	0 = DDI1 not detected 1 = DDI1 detected
GPIO_SUS[2]	Top Swap (A16) override	Weak internal (20k PU)	0 = Change Boot Loader address 1 = Normal Operation
GPIO_SUS[3]	MIPI-DSI Display Detect	Weak internal (20k PD)	0 = DSI Port not detected 1= DSI Port detected **Note:** DSI is not POR for BSW. This strap will not enable DSI on BSW. Leave the pin floating if GPIO functionality is not used.
GPIO_SUS[4]	Boot BIOS Strap (BBS)	Weak internal (20k PU)	0 = No SPI (Default) 1 = SPI
GPIO_SUS[5]	Flash Descriptor Security Override	Weak internal (20k PU)	0 = Not supported 1 = Normal Operation
GPIO_SUS[6]	Halt Boot Strap	Weak internal (20k PU)	1= Normal Operation **Note:** This strap **MUST** be High at RSMRST_N de-assert to ensure proper platform operation and use of GPIO_DFX[8:0]
GPIO_SUS[8]	PLLs, ICLK, USB2, DDI SFR Supply Select	Weak internal (20k PU)	0 = Supply is 1.25V 1 = Supply is 1.35V
GPIO_SUS[9]	ICLK, USB2, DDI SFR Bypass	Weak internal (20k PD)	0 = No bypass 1 = Bypass with 1.05V
GPIO_CAMERASB08	ICLK Xtal OSC Bypass	Weak internal (20k PD)	0 = No Bypass (Default) 1 = Bypass
GPIO_CAMERASB09	CCU SUS RO Bypass	Weak internal (20k PD)	0 = No Bypass (Default) 1 = Bypass
GPIO_CAMERASB11	RTC OSC Bypass	Weak internal (20k PD)	0 = No Bypass (Default) 1 = Bypass

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Title

MCP (OTHER)

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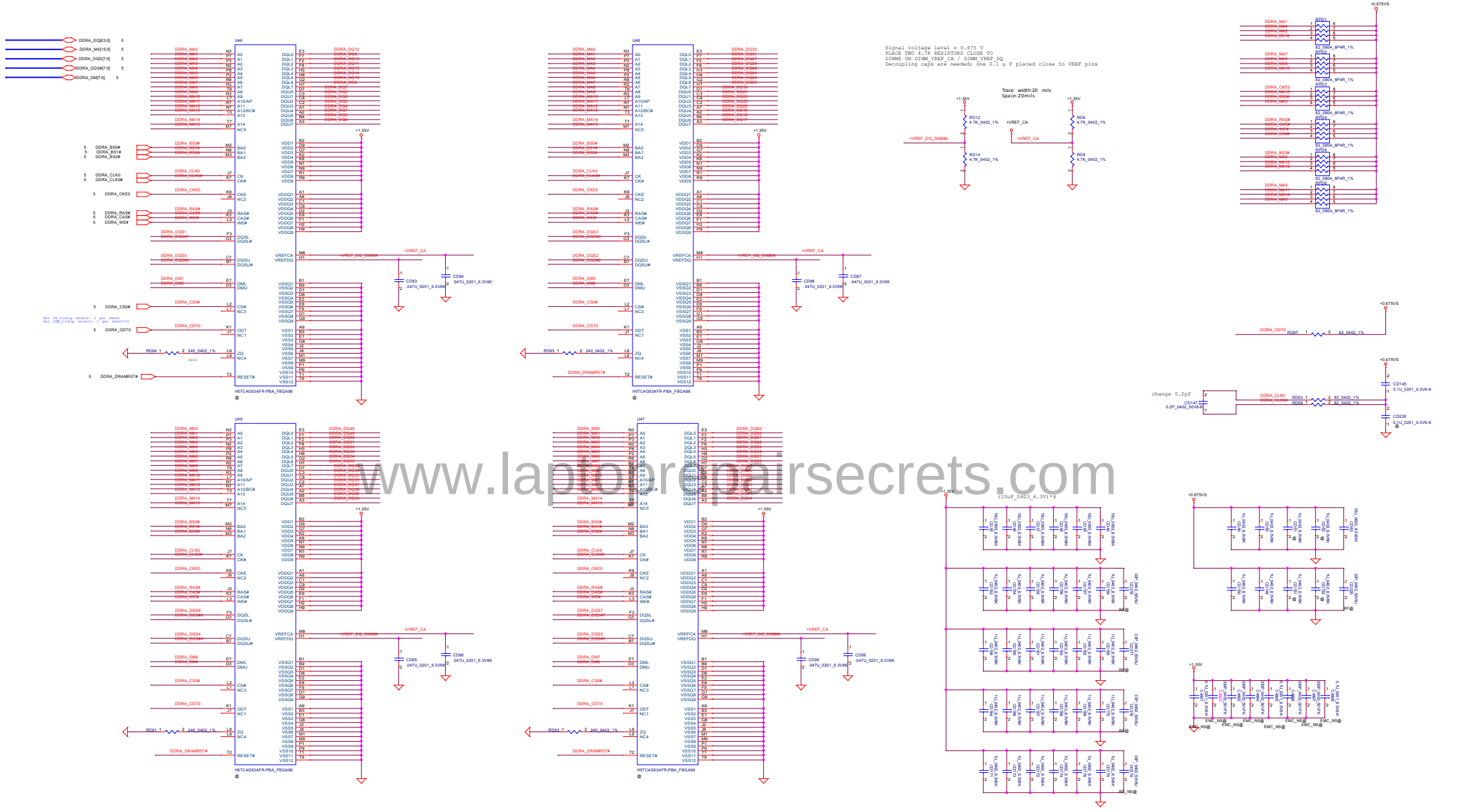

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Title: DDRIII SO-DIMM A

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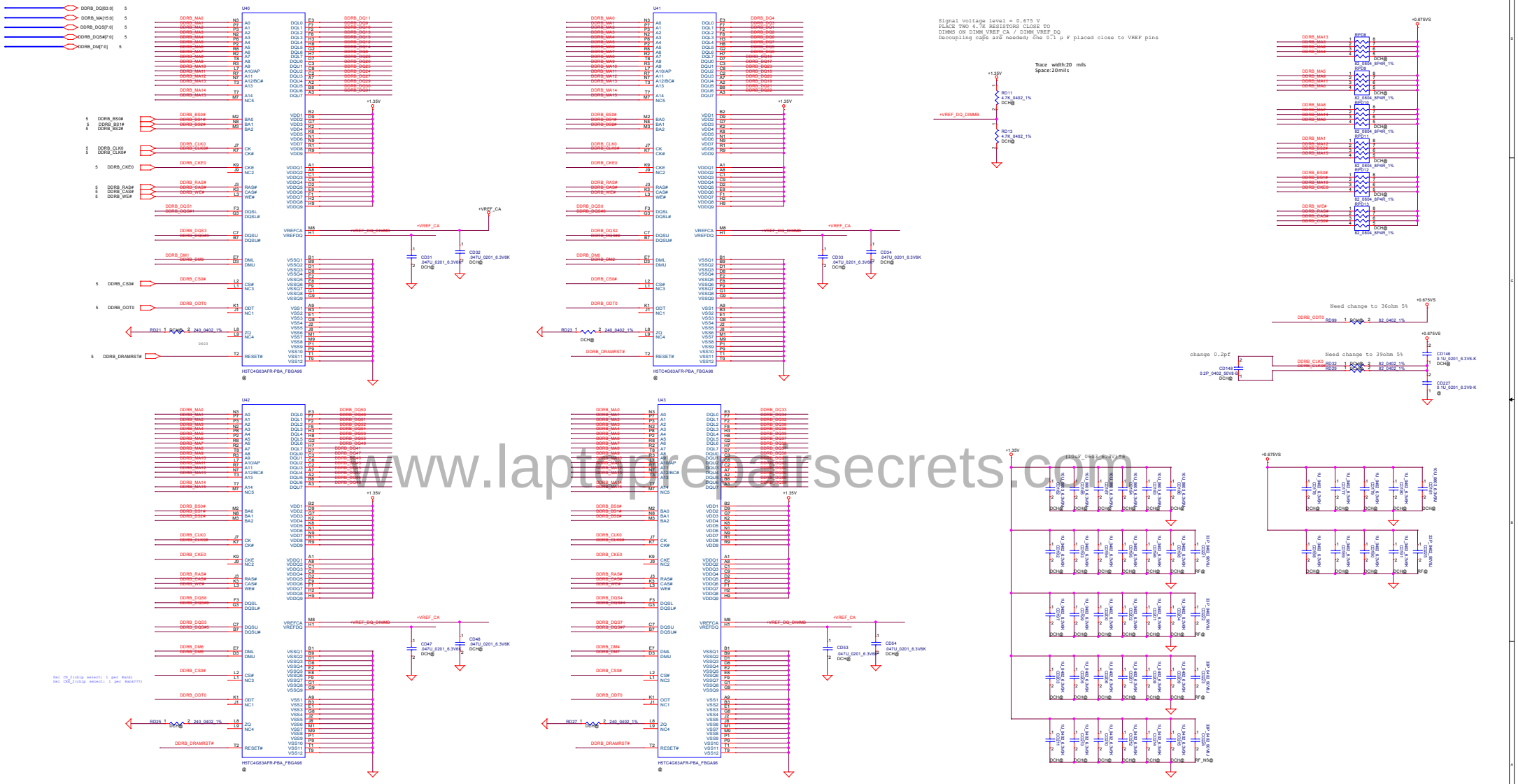

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USB Hub GL850G-OHY31

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Title

VGA Notes List

Size	Document Number	Rev
C	CGx20	0.1

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Title

N16X_PCIE/ DAC/ GPIO

Size	Document Number	Rev
Custom	CGx20	0.1

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Title

N16X_LVDS/ HDMI/ THERM

Size	Document Number	Rev
C	*CGx20*	0.1

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Title
N16X_Power

Size	Document Number	Rev
C	CGx20	0.1

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Title

N16X_+VGA CORE, GND

Size	Document Number	Rev
C	*CGx20*	0.1

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Title: ***N16X_MEM Interface***

Size	Document Number	Rev
C	CGx20	0.1

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Title: ***DDR3 VRAM Rank0_L***

Size	Document Number	Rev
C	*CGx20*	0.1

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Title: ***DDR3 VRAM Rank0_H***

Size	Document Number	Rev
C	***CGx20***	0.1

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Title: ***DDR3 VRAM Rank1_L***

Size	Document Number	Rev
C	***CGx20***	0.1

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Title: ***DDR3 VRAM Rank1_H***

Size	Document Number	Rev
C	*CGx20*	0.1

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Title: ***N16X_MISC***

Size	Document Number	Rev
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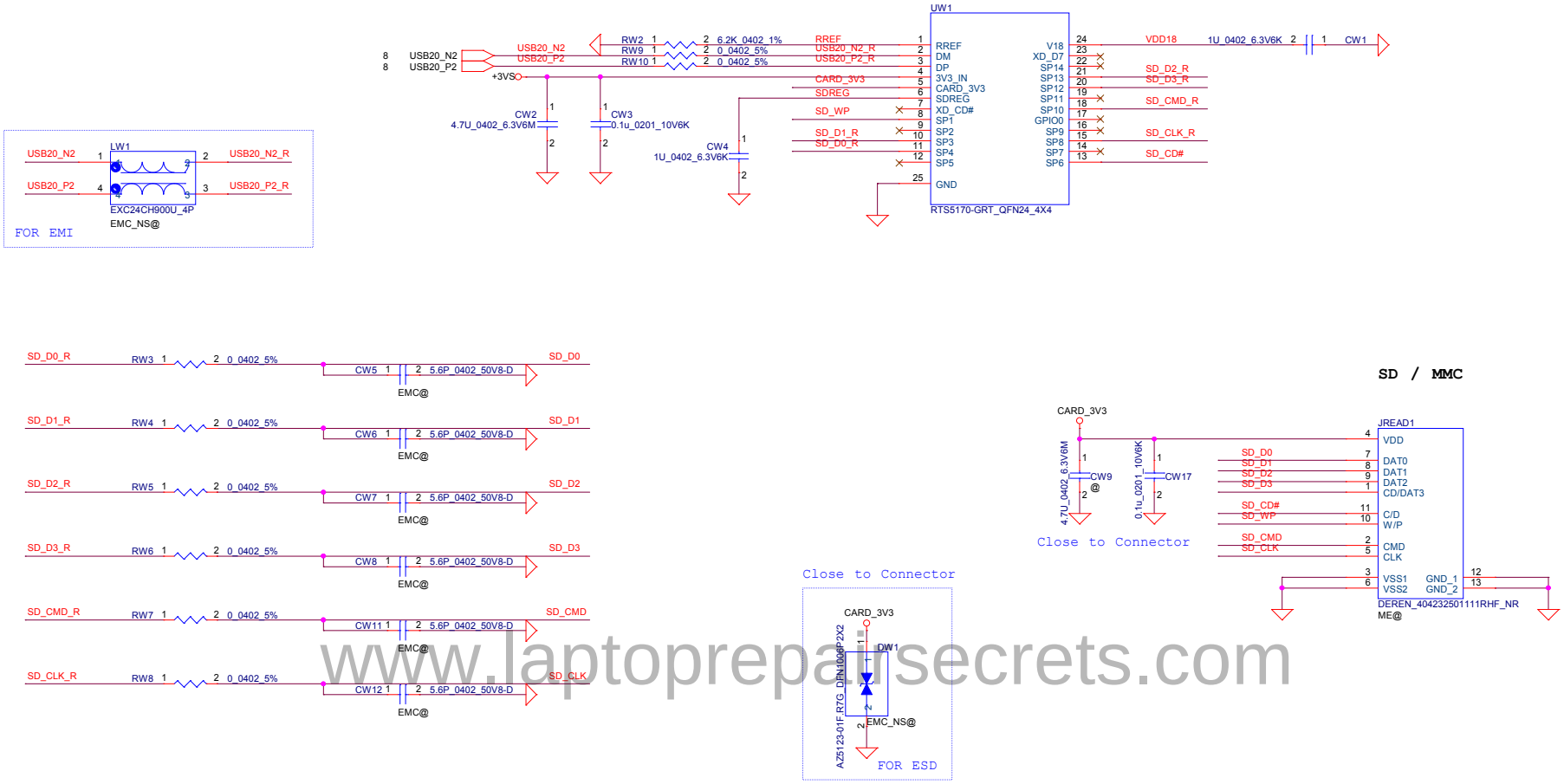
Title: ***Blank***

Size	Document Number	Rev
Custom	CGx20	0.1

Date: Tuesday, December 08, 2015 Sheet 29 of 57



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Title: **Cardreader**

Size	Document Number	Rev
Custom	CGx20	0.1

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Title

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Size	Document Number	Rev
Custom	CGx20	0.1

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Title

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Size	Document Number	Rev
Custom	CGx20	0.1

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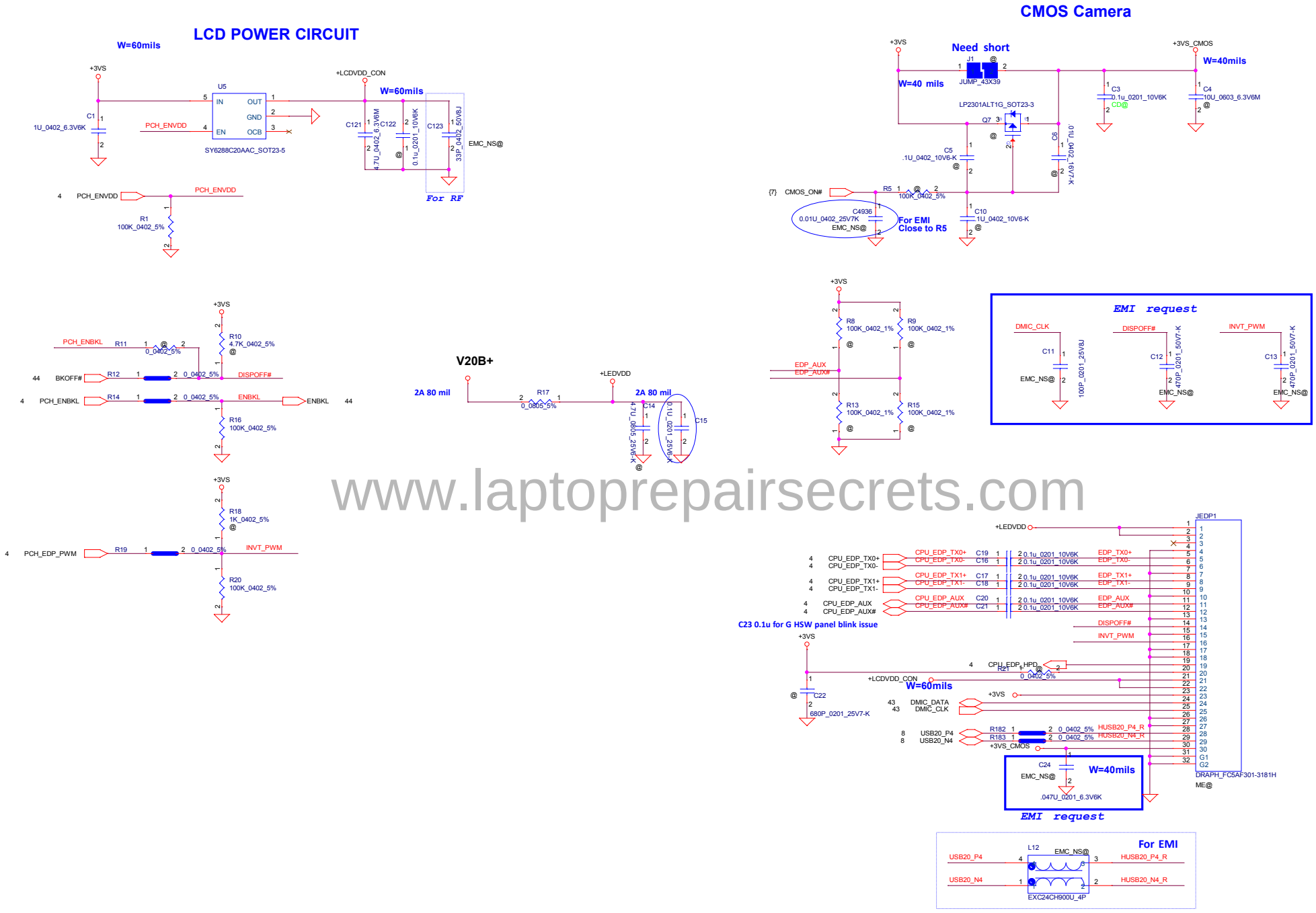

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Title: *eDP/ CMOS/Touch screen*

Size	Document Number	Rev
Custom	CGx20	0.1

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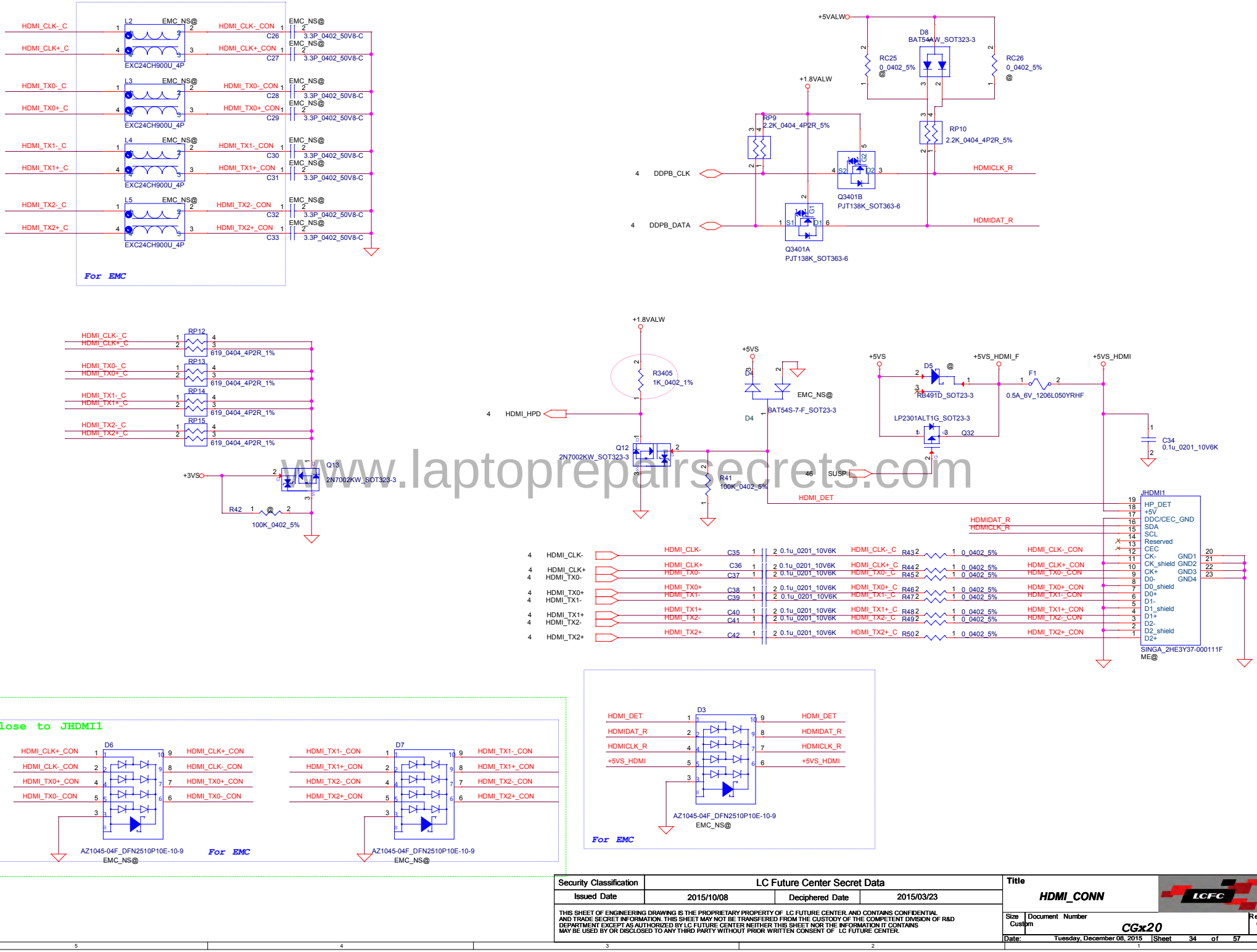

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Issued Date	2015/10/08	Deciphered Date	2015/03/23

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Title	HDMI_CONN		
Size	Document Number		Rev
Custom	CGx20		0.1
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Title

DP to CRT Convert(IT6515FN)

Size	Document Number	Rev
Custom	CGx20	0.1

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Title: *CRT*

Size	Document Number	Rev
Custom	*CGx20*	0.1

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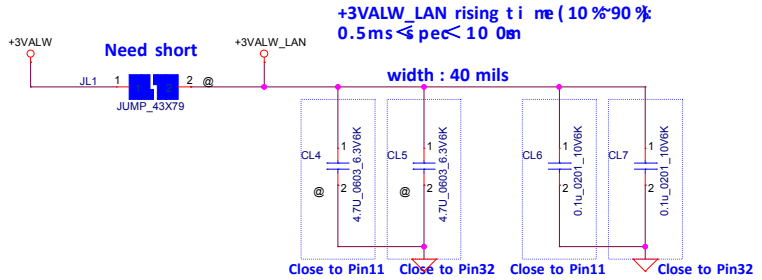
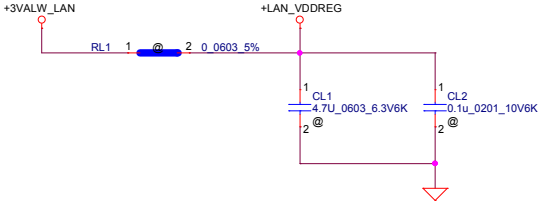
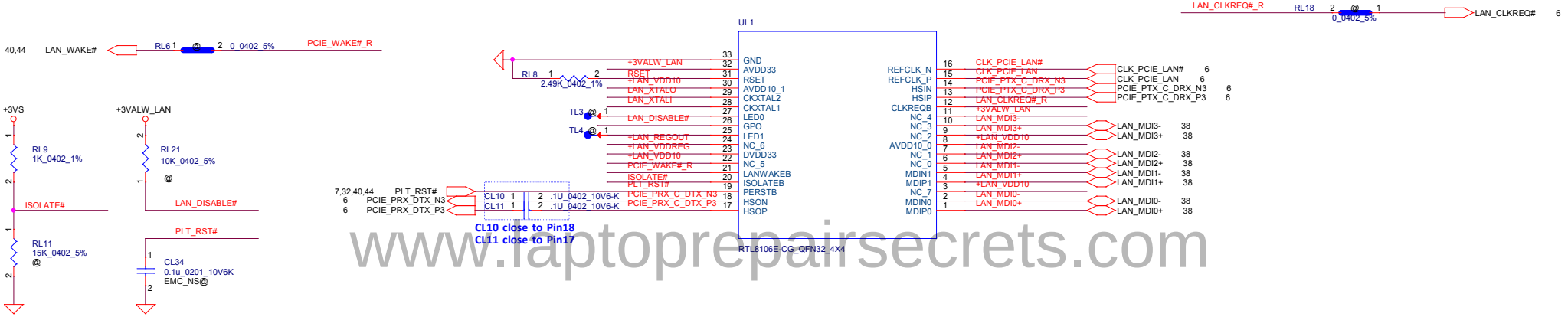
+3VALW TO +3VALW_LAN







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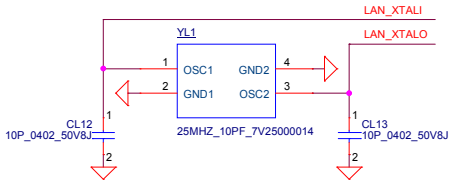
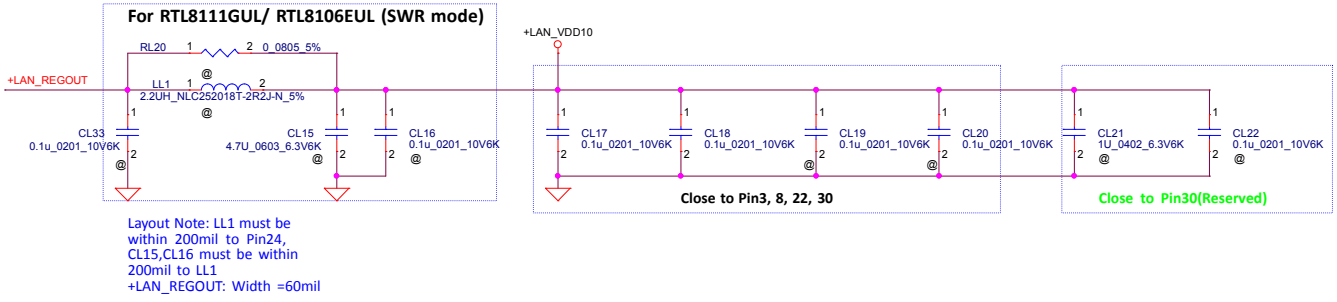
100M LAN





Layout Note: LL1 must be within 200mil to Pin24, CL15,CL16 must be within 200mil to LL1
+LAN_REGOUT: Width =60mil

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Title: LAN_RTL8106EUL

Size	Document Number	Rev
Custom	CGx20	0.1

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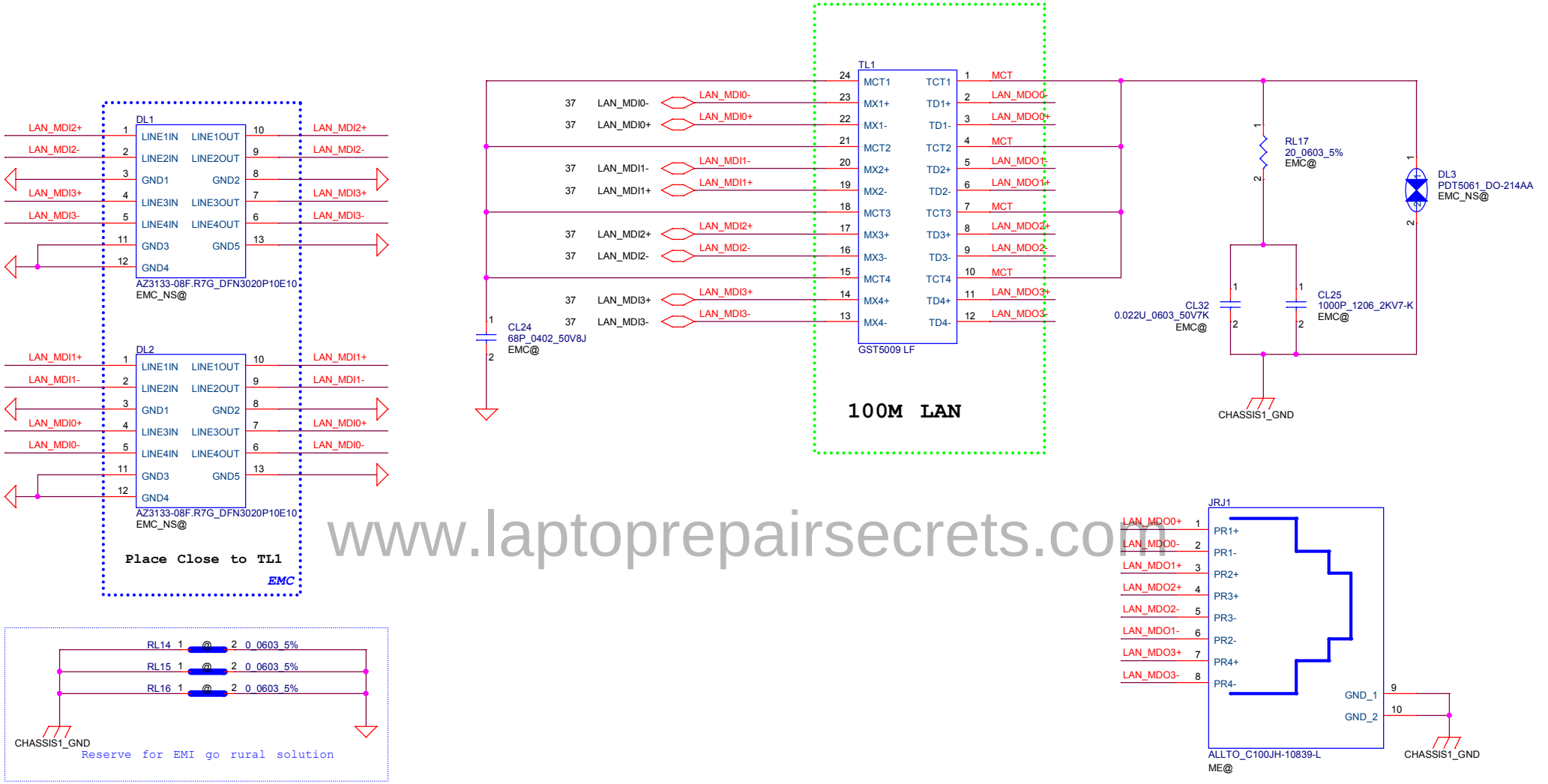

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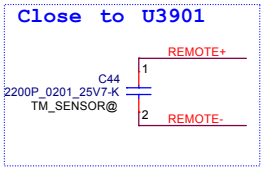
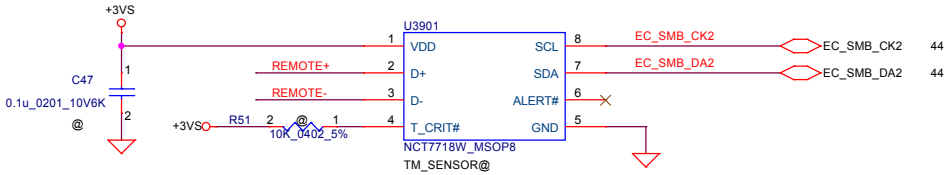
Title	LAN_Transformer		
Size	Document Number		Rev
B	CGx20		0.1
Date:	Tuesday, December 08, 2015	Sheet	38 of 57

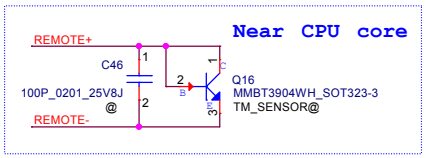


REMOTE+/-_R, REMOTE1+/-, REMOTE2+/-:
Trace width/space:10/10 mil
Trace length:<8"

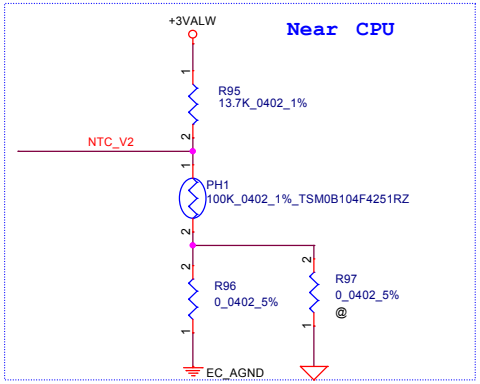
SMSC thermal sensor placed near DIMM



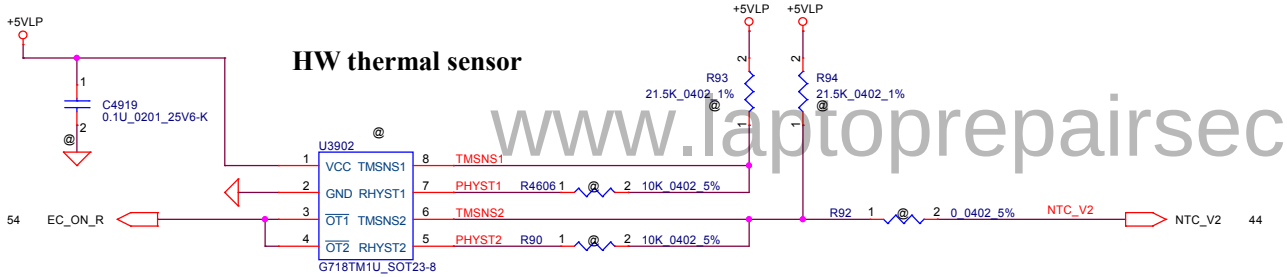
Address 1001_100xb



Baytrail SOC use thermal sensor to read the thermal,
Baytrail don't has PECI signal



HW thermal sensor



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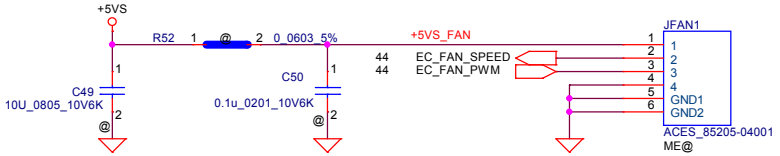
over temperature threshold:
RSET=3*RTMH
92+/-30C
Hysteresis temperature threshold.
RHYST=(RSET*RTML)/(3*RTML-RSET)
56+/-30C

FAN Conn



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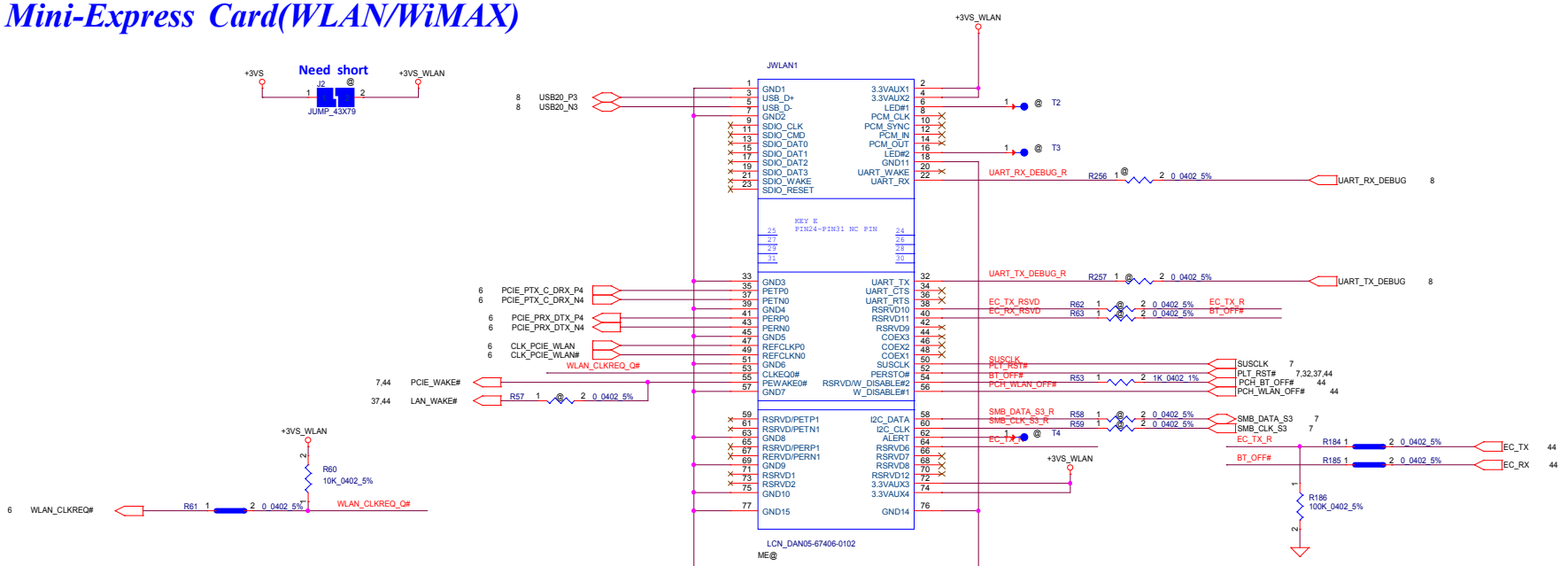
Title: *Thermal sensor/FAN CONN*

Size	Document Number	Rev
Custom	CGx20	0.1

Date: Saturday, December 12, 2015 Sheet 39 of 57

Mini-Express Card(WLAN/WiMAX)



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Title: **NGFF WLAN**

Size	Document Number	Rev
Custom	CGx20	0.1

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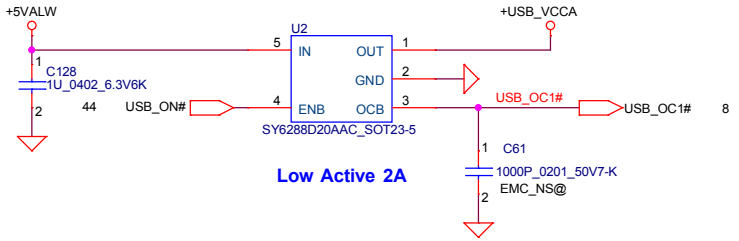
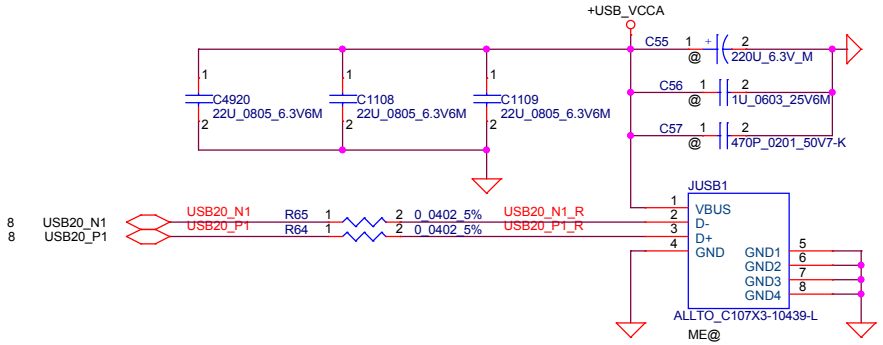
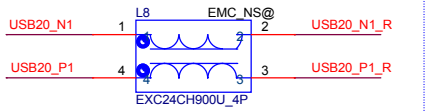
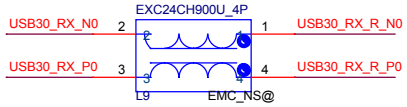
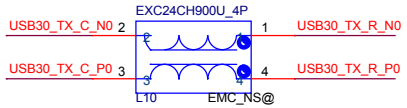
LEFT SIDE USB PORT X2

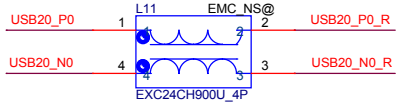












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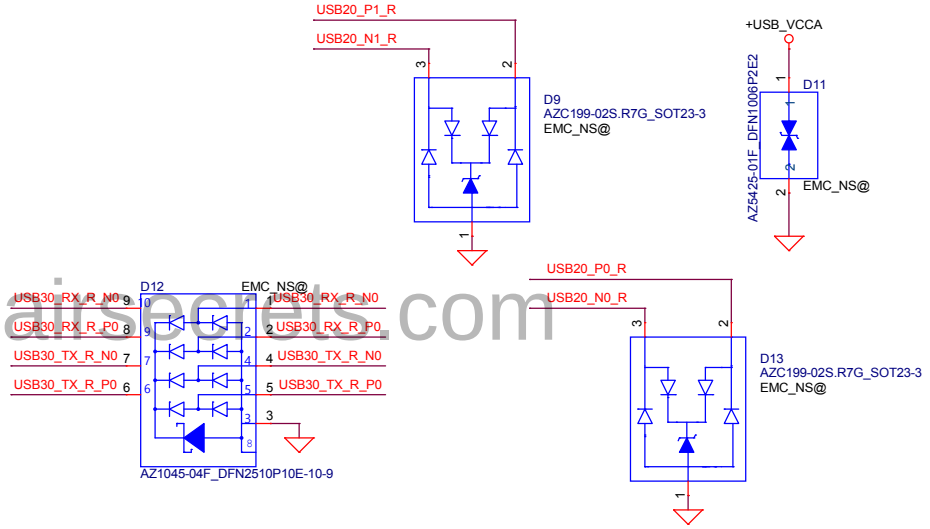

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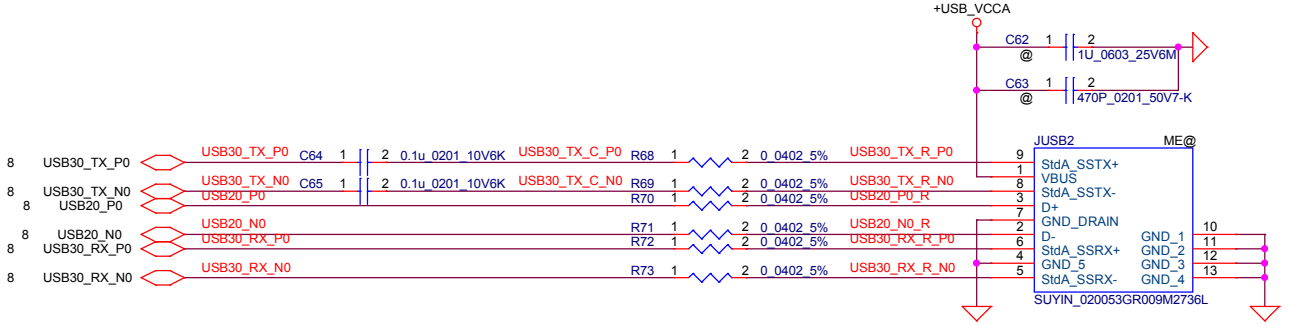

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Issued Date	2015/10/08	Deciphered Date	2013/08/05

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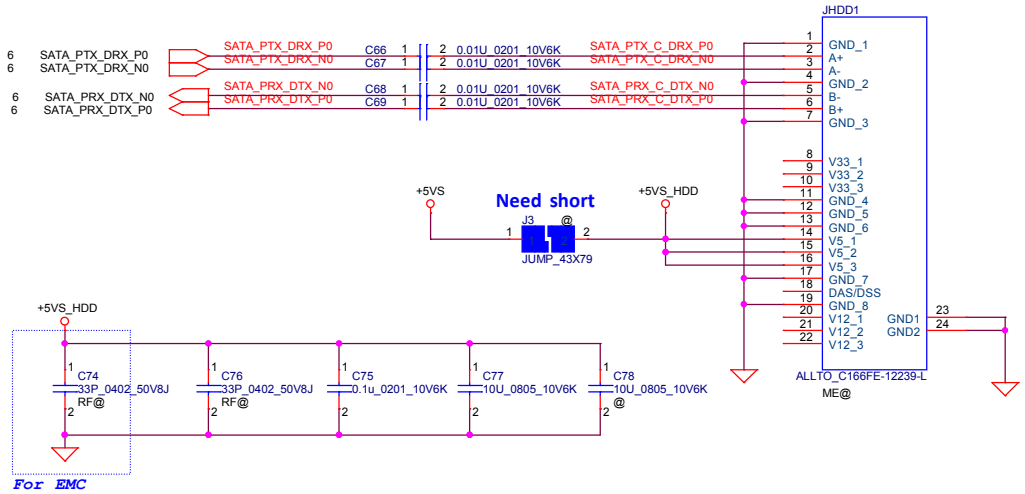
Title: **USB2.0/USB3.0 PORT (LEFT)**

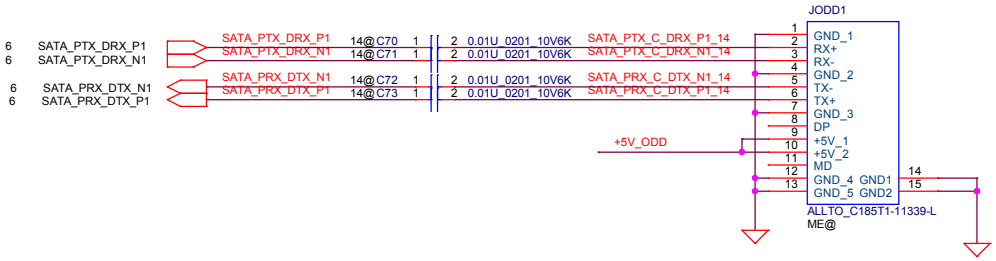
Size	Document Number	Rev
Custom	CGx20	0.1

Date: Tuesday, December 08, 2015 | Sheet 41 of 57

SATA HDD Conn.



FOR 14"
SATA ODD Conn.



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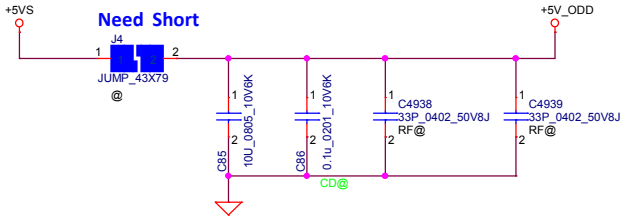

Security Classification	LC Future Center Secret Data		
Issued Date	2015/10/08	Deciphered Date	2015/03/23

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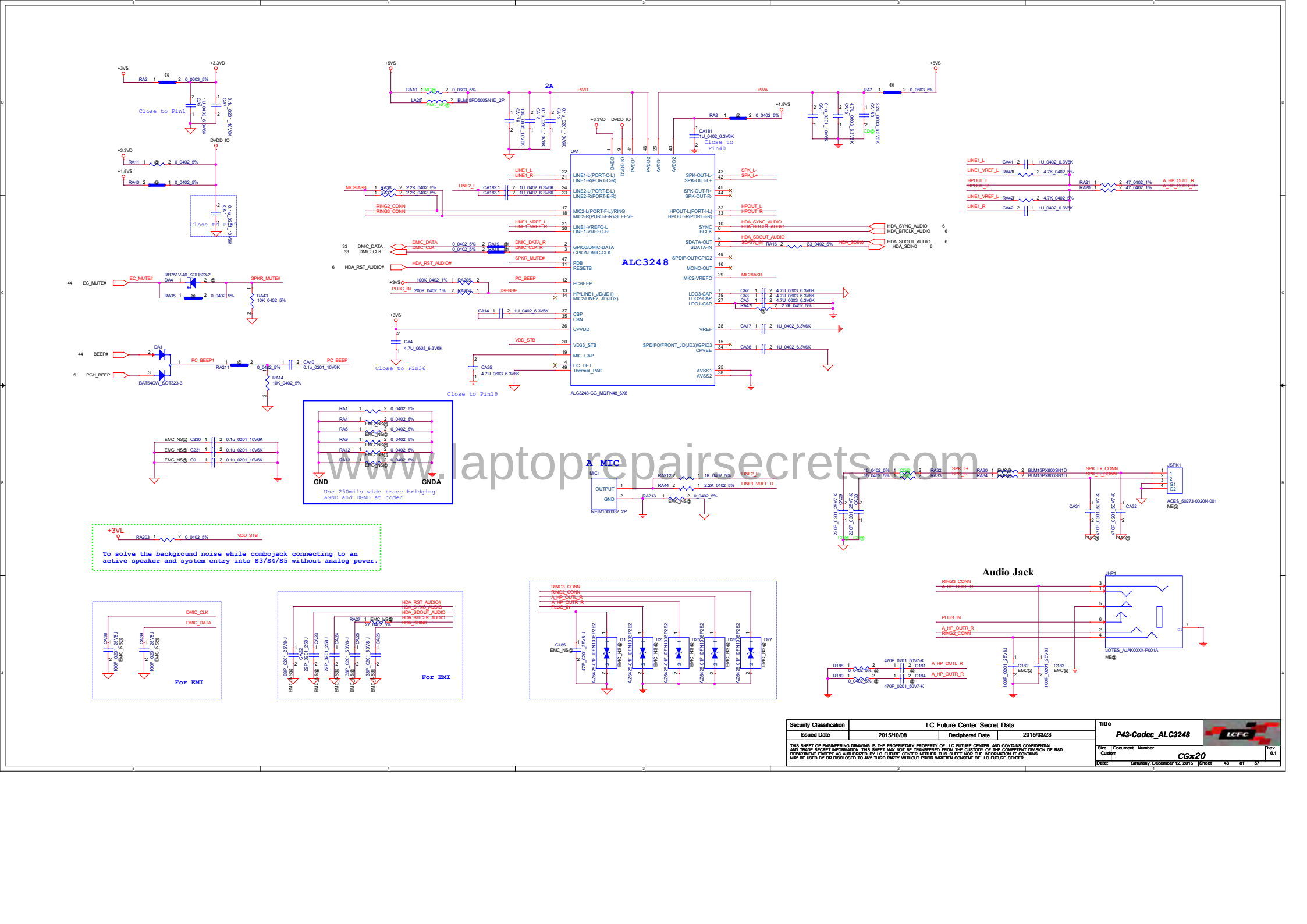
Title: **HDD/ODD CONN**

Size	Document Number	Rev
Custom	CGx20	0.1

Date: Saturday, December 12, 2015 Sheet 42 of 57



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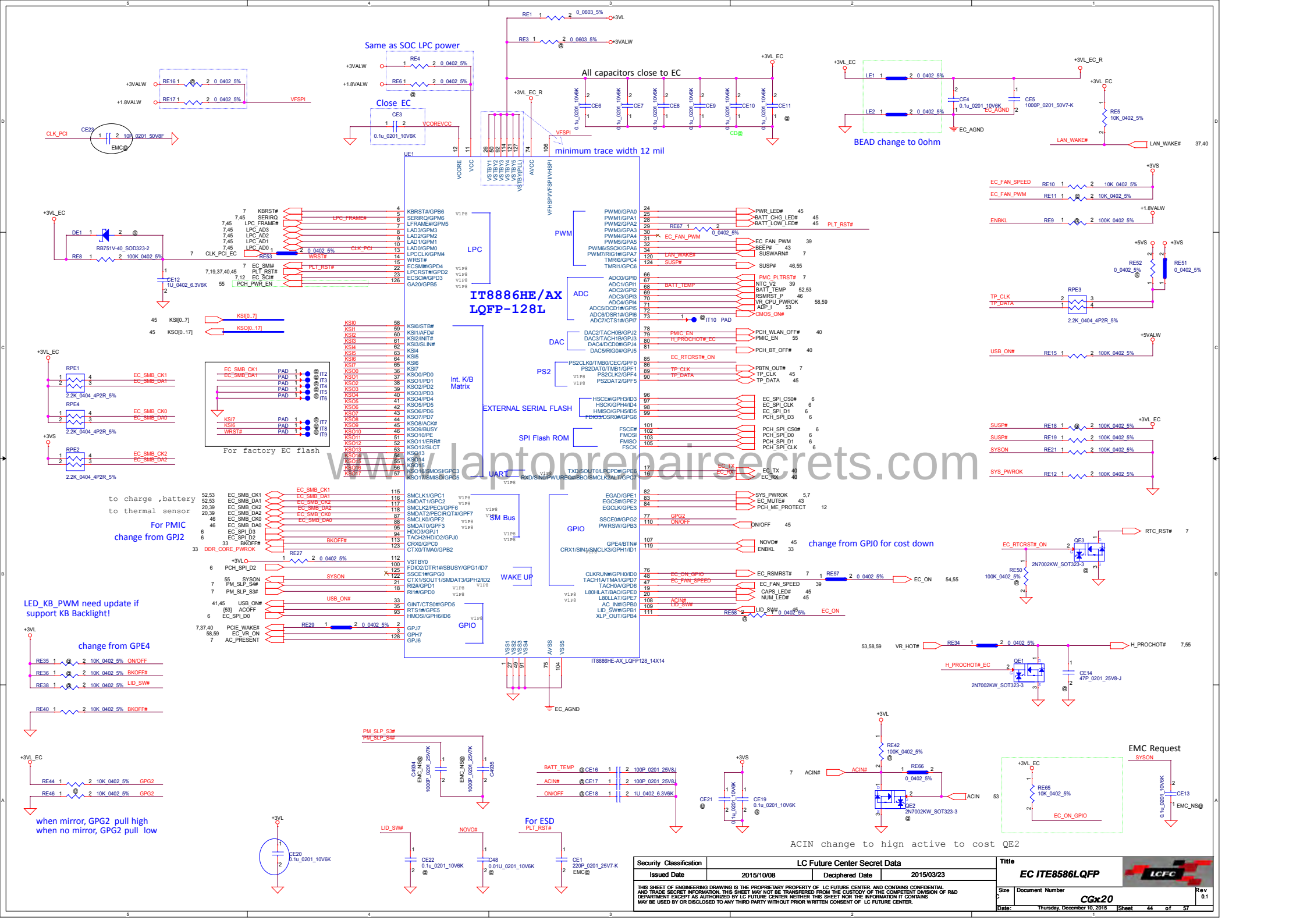

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Issued Date	2015/10/08	Deciphered Date	2015/03/23

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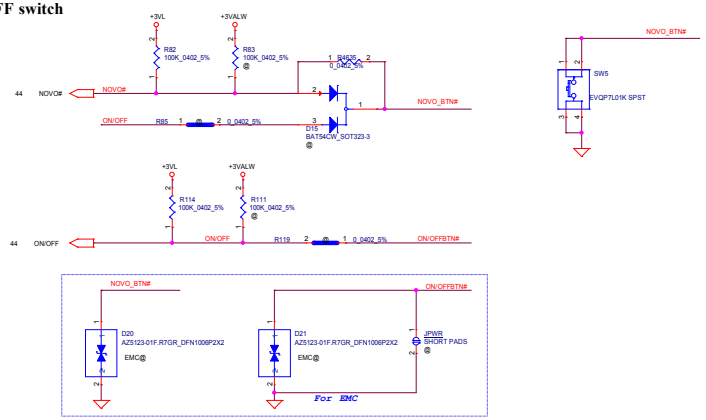
Title: EC ITE8586LQFP

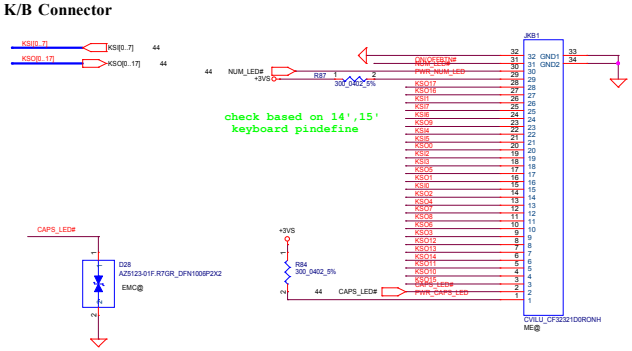
Size	Document Number	Rev
C	CGx20	0.1

Date: Thursday, December 10, 2015 Sheet 44 of 57

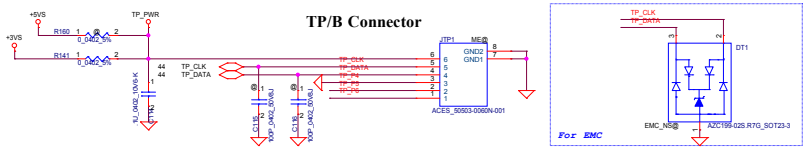
ON/OFF switch



K/B Connector



TP/B Connector



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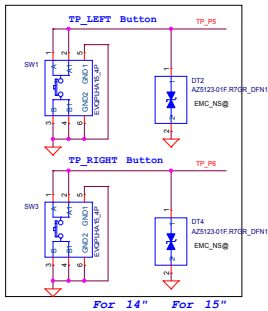
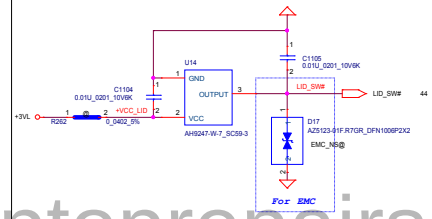
5	CLK
4	DAT
3	GND
2	TP-L
1	TP-R



Hall Sensor



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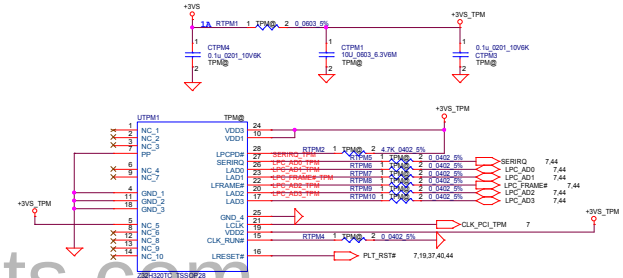

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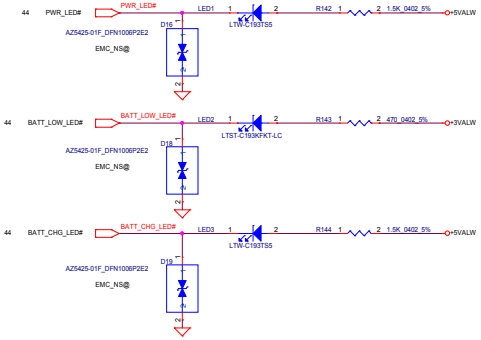

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Security Classification	LC Future Center Secret Data		
Issued Date	2015/10/08	Deciphered Date	2015/03/23

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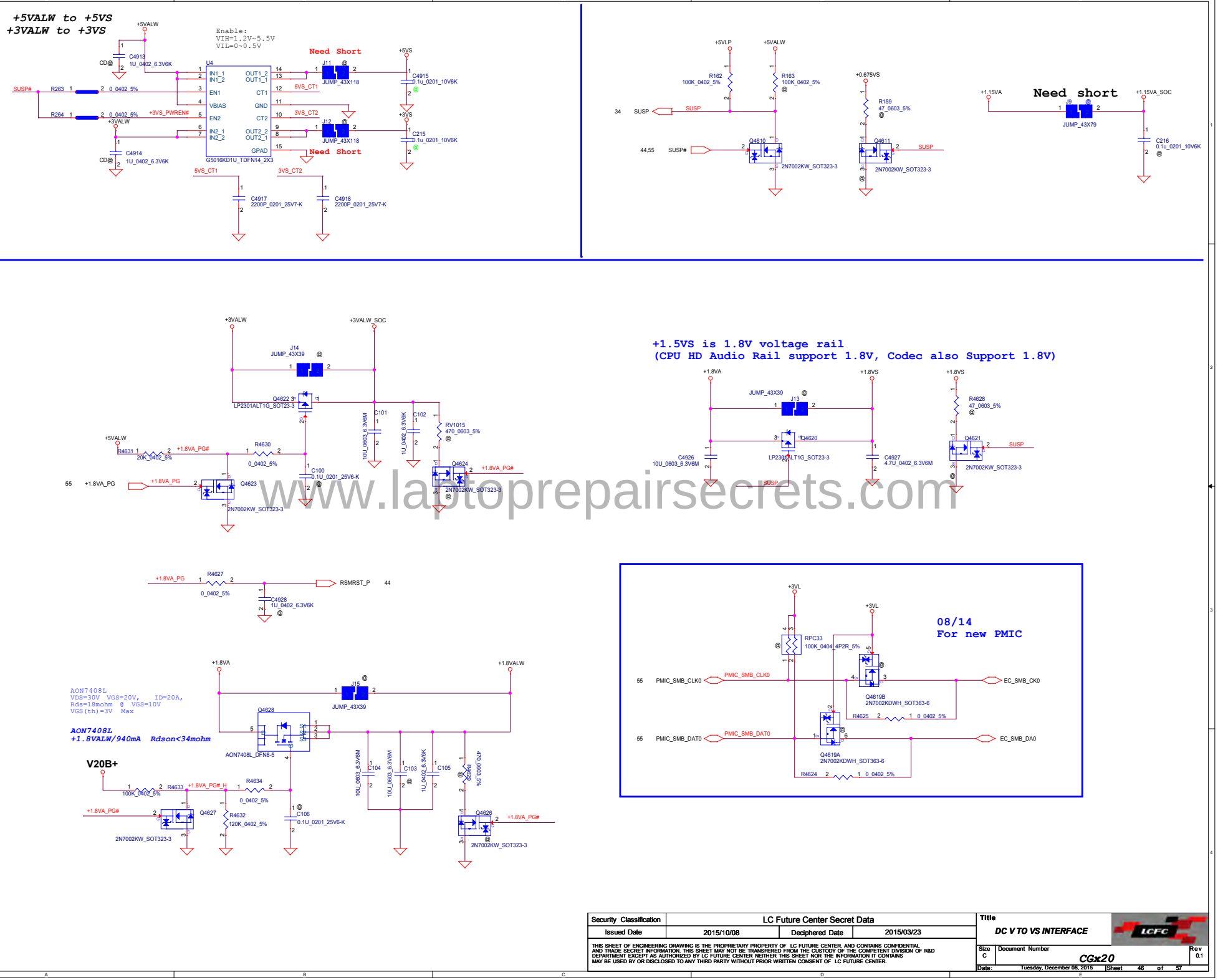
Title: KBD/PWR/IO/LED/TP Conn.

Size	Document Number	Rev
Custom	CGx20	0.1

Date: Friday, December 11, 2015 Sheet 45 of 57



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Issued Date	2015/10/08	Deciphered Date	2015/03/23

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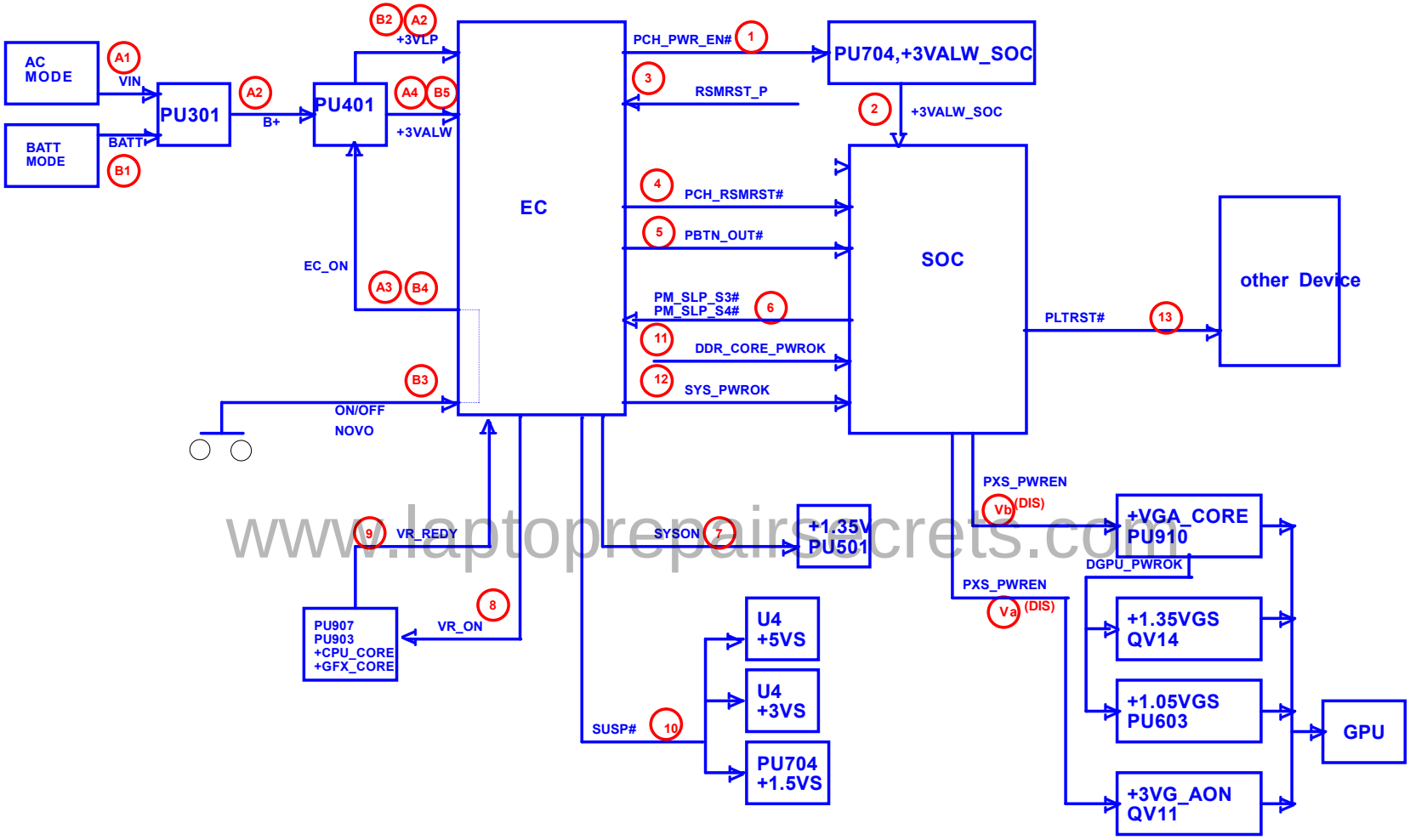
Title: DC V TO VS INTERFACE

Size	Document Number	Rev
C	CGx20	0.1

Date: Tuesday, December 08, 2015 Sheet 46 of 57



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Issued Date	2015/10/08	Deciphered Date	2015/03/23

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Title: *Power sequence Block*

Size	Document Number	Rev
Custom	CGx20	0.1

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Issued Date	2015/10/08	Deciphered Date	2015/03/23

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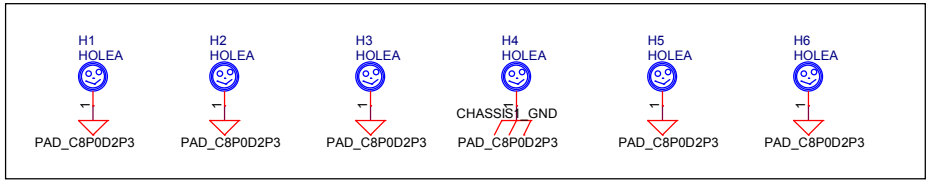
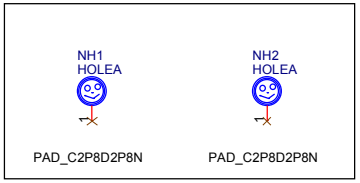
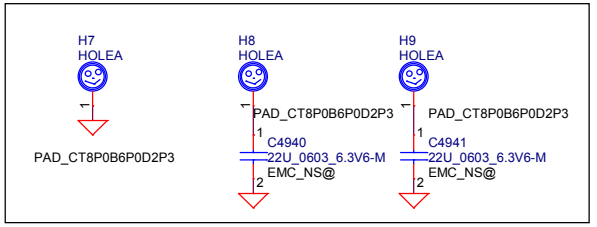
Title: *Virtual symbol*

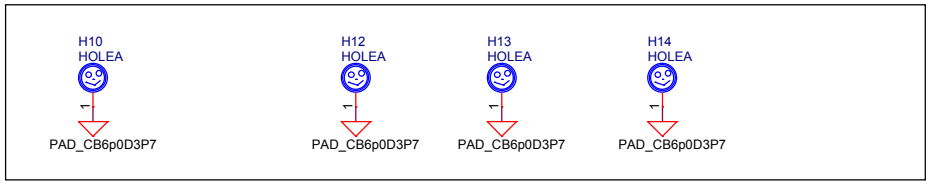
Size	Document Number	Rev
C	CGx20	0.1

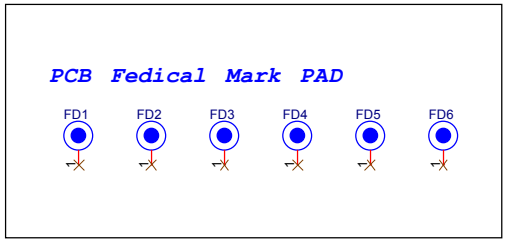
Date: Tuesday, December 08, 2015 Sheet 48 of 57











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Issued Date	2015/10/08	Deciphered Date	2015/03/23

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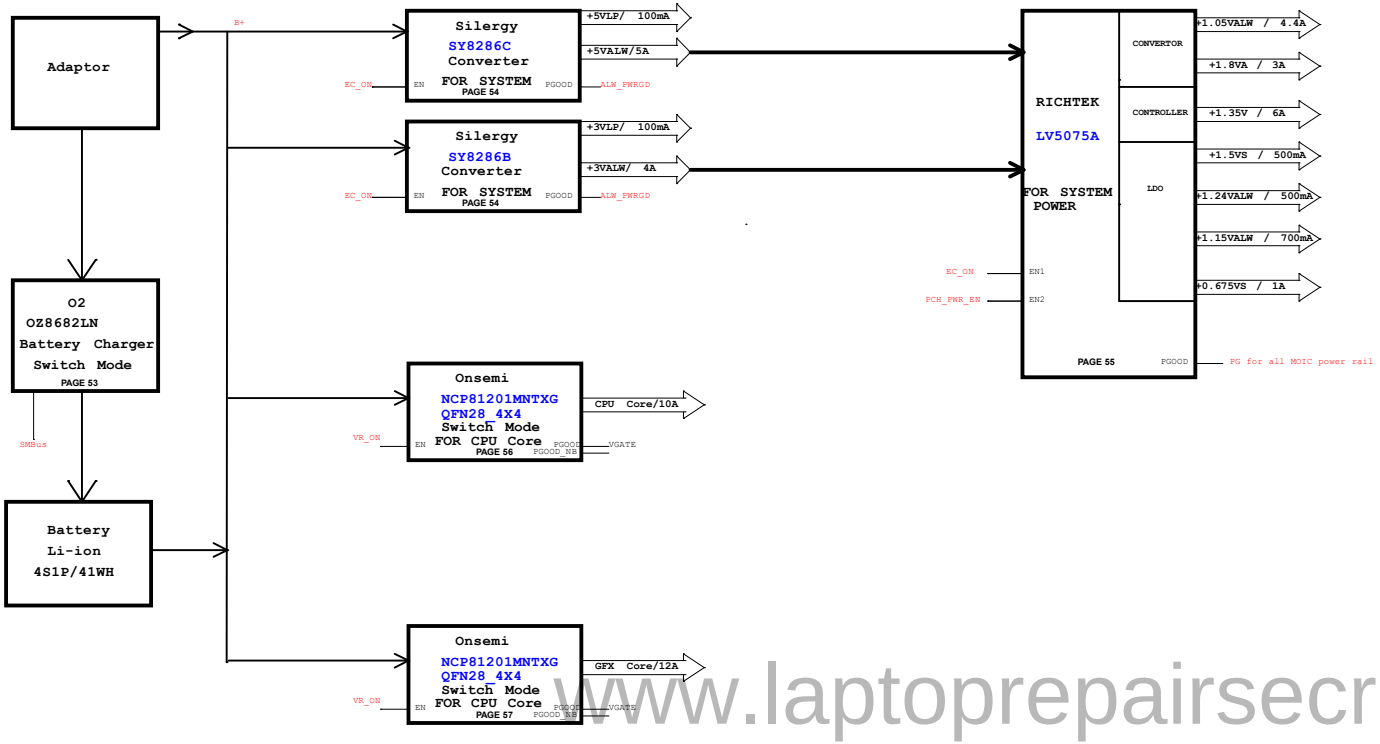
Title: *Hole*

Size	Document Number	Rev
B	CGx20	0.1

Date: Friday, December 11, 2015 Sheet 49 of 57



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Security Classification	LC Future Center Secret Data		
Issued Date	2013/08/06	Deciphered Date	2013/08/05

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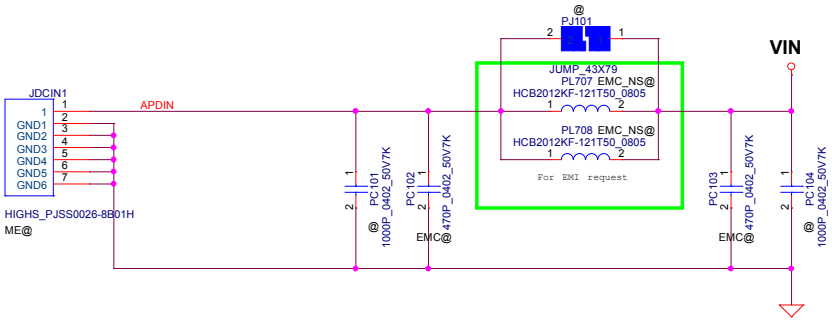
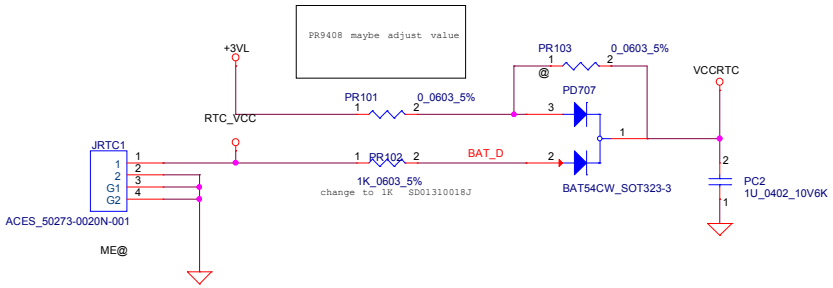
Title	Power Diagram	
Size: D	Document Number: CGx20	Rev: 0.1
Date: Wednesday, December 04, 2013	Sheet 50 of 57	





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Security Classification	LC Future Center Secret Data		
Issued Date	2015/02/27	Deciphered Date	20140213

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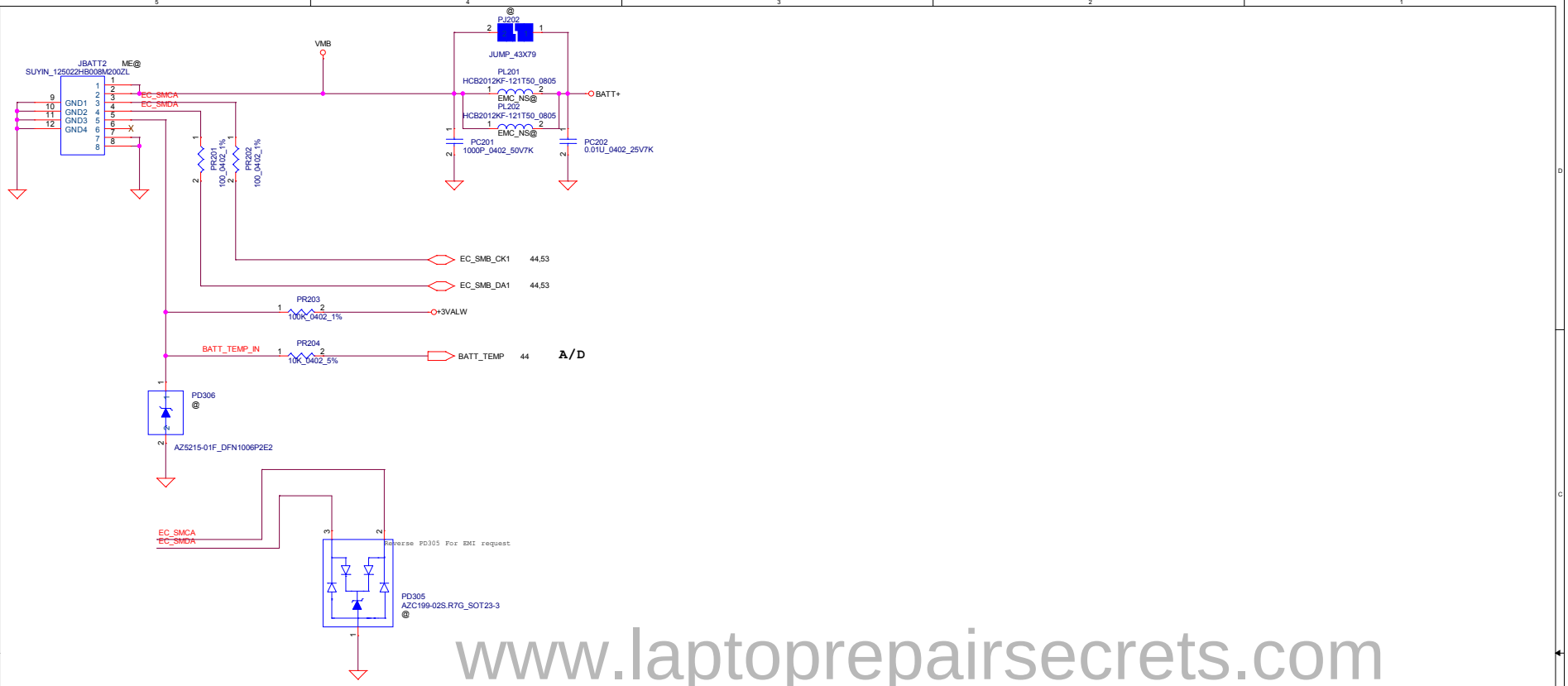
Title: **DCIN / RTC**

Size	Document Number	Rev
Custom	CGx20	0.1

Date: Wednesday, December 09, 2015 Sheet 51 of 57



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Security Classification	LC Future Center Secret Data		
Issued Date	2015/02/27	Deciphered Date	20140213

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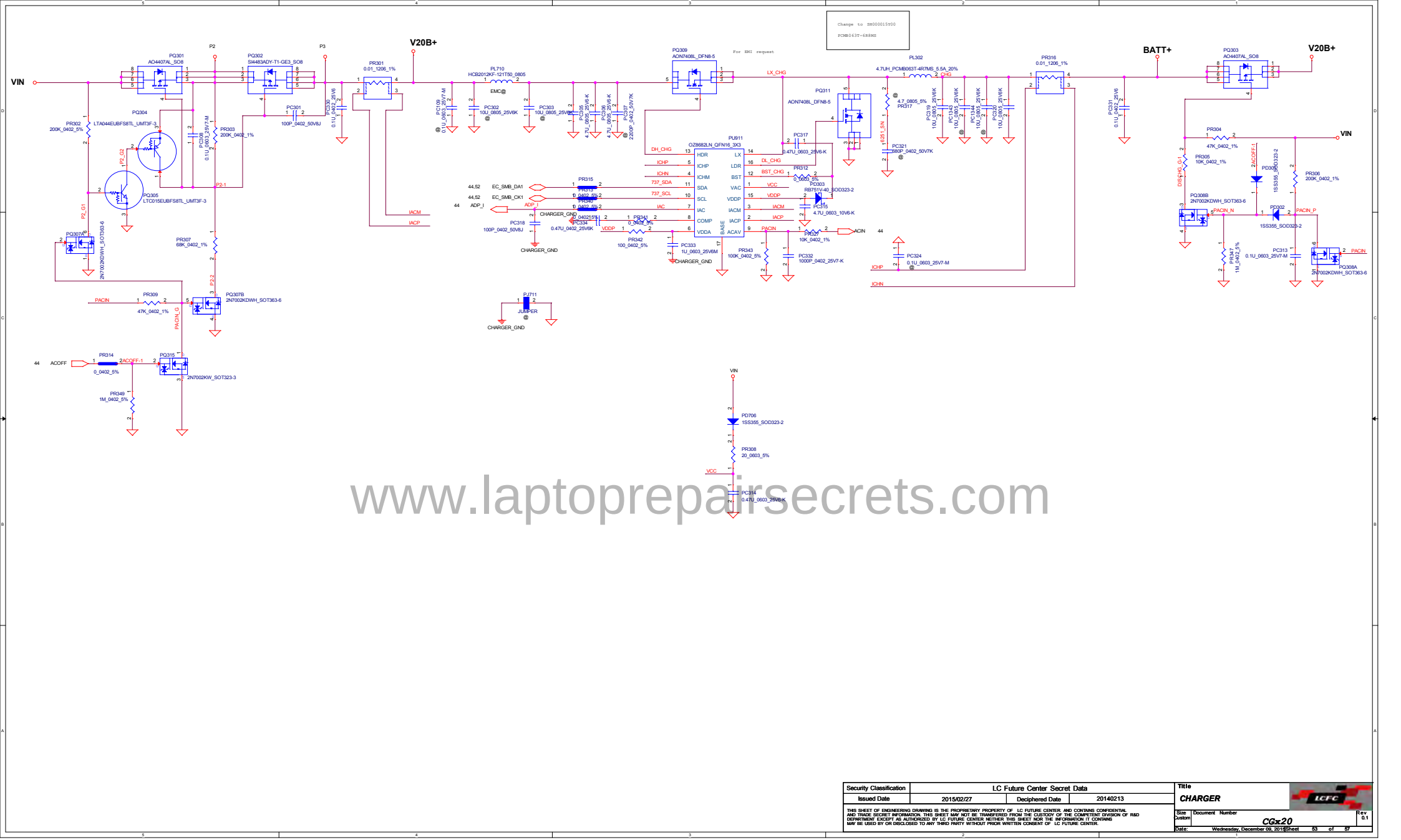
Title: **BATTERY CONN/OTP**

Size	Document Number	Rev
Custom	CGx20	0.1

Date: Wednesday, December 08, 2015 Sheet 52 of 57



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Security Classification	LC Future Center Secret Data		
Issued Date	2015/02/27	Deciphered Date	20140213

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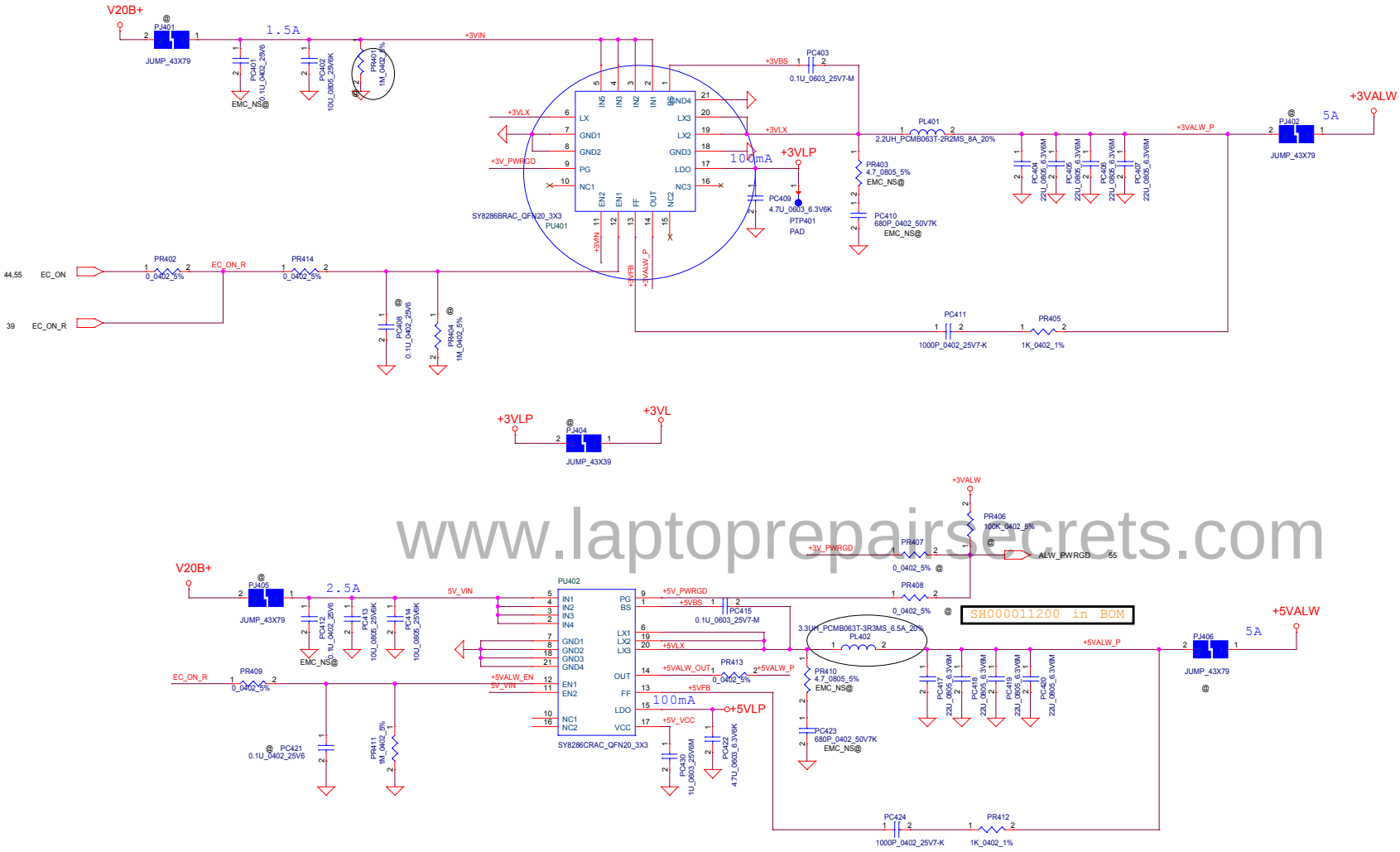
Title: CHARGER

Size	Document Number	Rev
Custom	CGx20	0.1

Date: Wednesday, December 09, 2015 Sheet 53 of 57



Security Classification	LC Future Center Secret Data		
Issued Date	2013/08/08	Deciphered Date	2013/08/05

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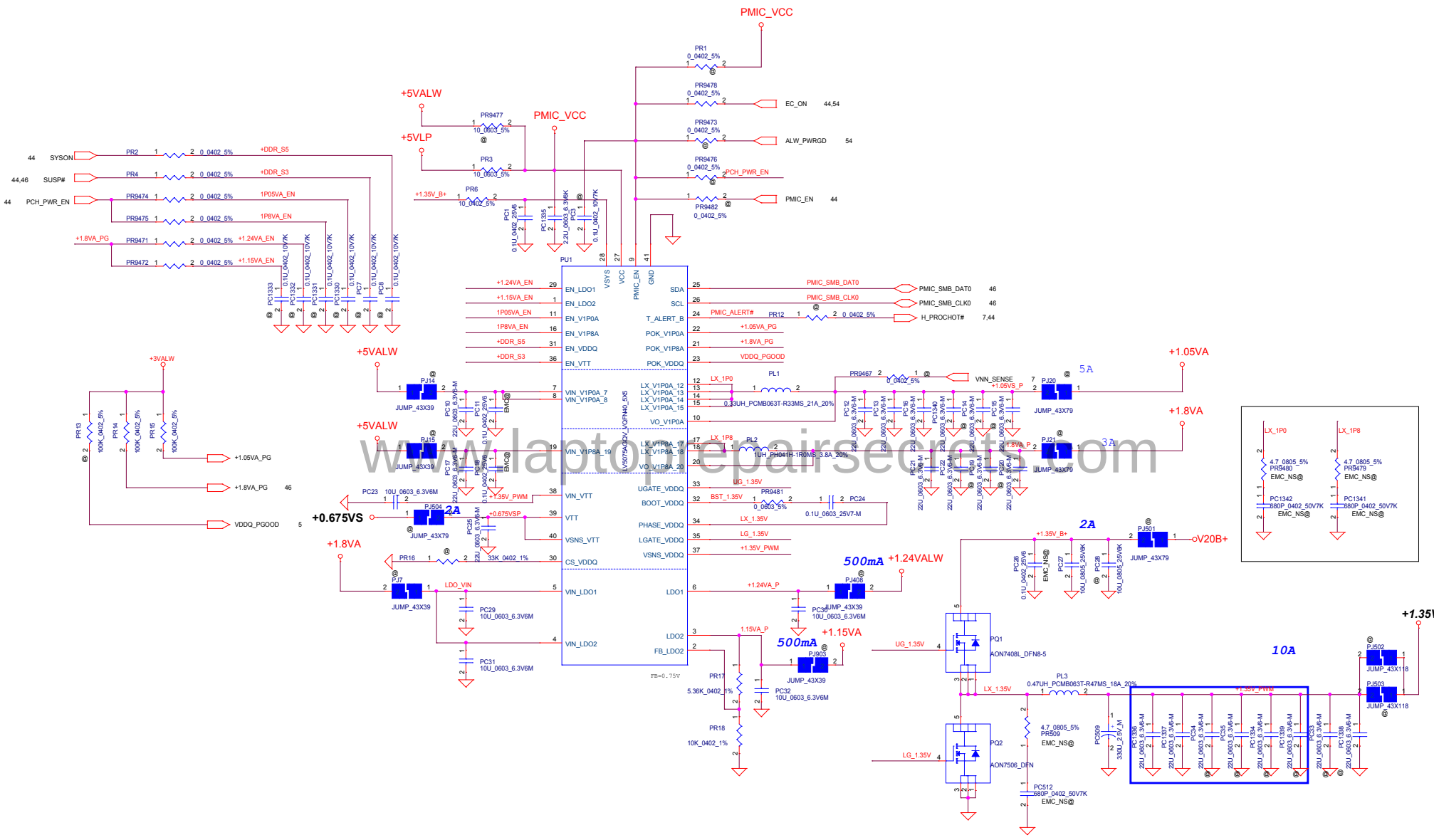
Title: PWR_3VALW/5VALW

Size	Document Number	Rev
C	CGx20	0.1

Date: Wednesday, December 09, 2015 Sheet 54 of 57



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Security Classification	LC Future Center Secret Data		
Issued Date	2013/08/08	Deciphered Date	2013/08/05

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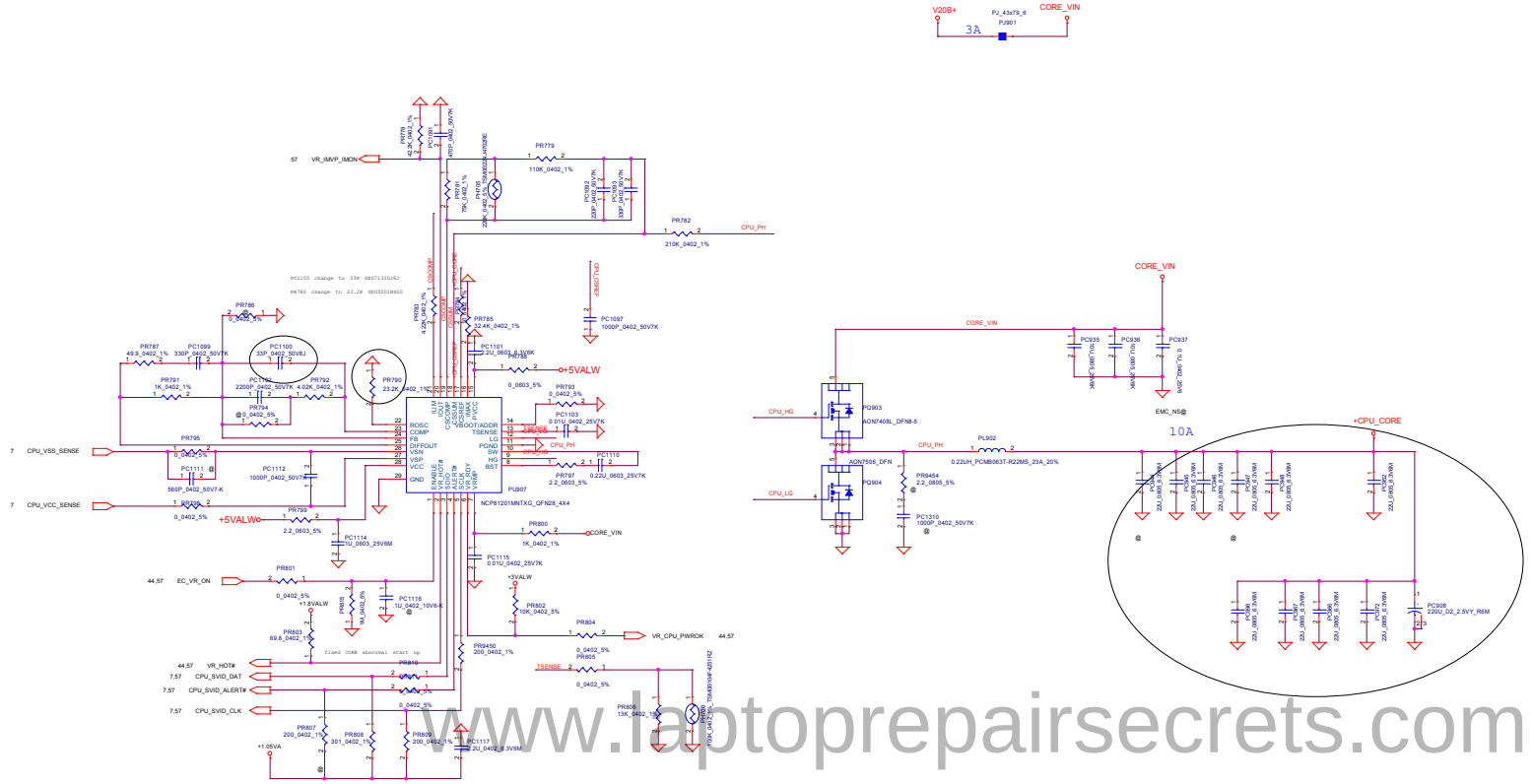
Title: **PWR-SYSTEM POWER**

Size	Document Number	Rev
C	CGx20	1.0

Date: Wednesday, December 09, 2015 Sheet 55 of 57



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Security Classification	LC Future Center Secret Data		Title
Issued Date	2012/09/03	Deciphered Date	2012/09/03
			PWR_CPU_CORE/GFX_CORE
Size	Document Number		Rev
D	CGx20		0.3
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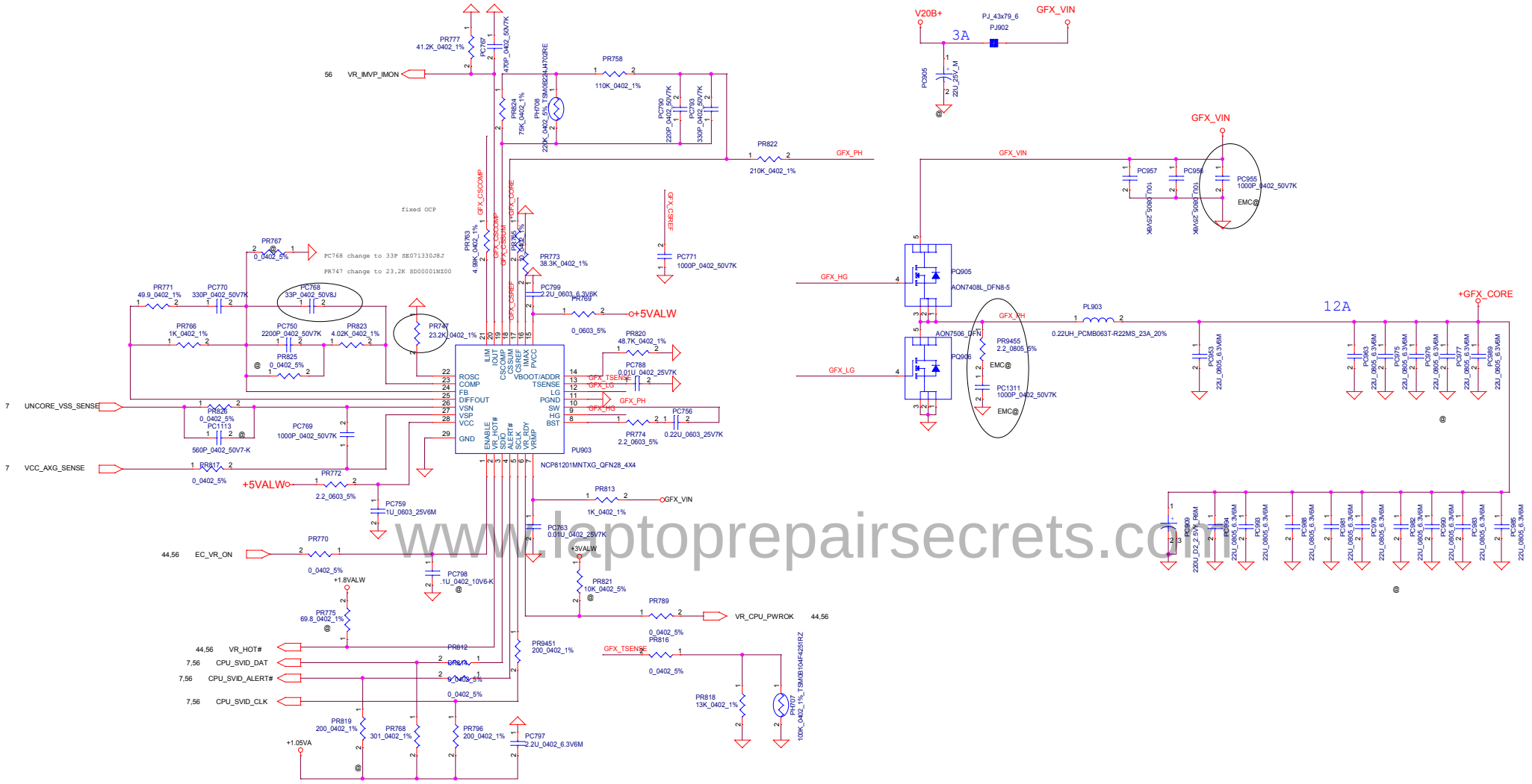

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